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(Click - Python Command Line Utility 7.0)

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(tcpdump 4.9.2)

<torsten@debian.org> and Romain Francoise <rfrancoise@debian.org>.

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(pycparser 2.19)
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(python-cryptography 2.3.1)

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(Shadow Tool Suite 4.1.4.2+svn3283)

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(python-werkzeug 0.12.2)

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(libpcap 1.9.0)

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(OpenSSH 7.4p1)

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(EventLog Library 0.2.12)

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(Flask 0.12.2)

Source: https://pypi.python.org/pypi/Flask

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(sg3_utils 1.44)

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(libevent - an event notification library 2.1.8-stable)

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(psutil 5.5.0)

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(Mosquitto 1.5.8)

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(libgpg-error 1.33)

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was
made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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(libjwt 1.9.0, libtalloc1 2.1.8)

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Version 3, 29 June 2007

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(pypi/setuptools 40.8.0)

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(Python six 1.12.0)

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(color-string 0.6.0)

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(Qix-/color-convert 1.9.3)

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(Qix/color 2.4.1)

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(python-jsonschema 2.5.1)

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(parse5 5.1.1)

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(asn1crypto 0.24.0)

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(libxml2 2.9.9)

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(@ngx-translate/http-loader v4.0.0, ngx-translate/core v11.0.1)

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(moment/moment 2.24.0)

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(card10-sys 1.9.0)

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(zsh 5.6.2)

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(parson 20180104-snapshot-c147744b)

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(libffi 3.3-rc0)

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(python-cffi 1.11.2)

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(Zone.js 0.10.3)

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(@angular/flex-layout 9.0.0-beta.31)
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(xmlbuilder-js v11.0.1)

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(hammer.js 2.0.8)

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(MartijnBraam/python-isc-dhcp-leases 0.9.0)

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(ricmoo/pyaes 20170920-snapshot-23a1b4c0)

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(color-name 1.1.3, color-name 1.1.4)

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(PyJWT 1.4.2)

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(mobx 4.14.1)

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(pypi/setuptools 40.6.3, Python programming language 3.7.4)

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(debianutils 4.8.1)

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(curl 7.65.3)

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(zlib 1.2.11)

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else {  
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quiet_interface_discovery = 1;  
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&copy, file, line); return NULL;
&copy->address,
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&copy->bindings; for (ia = lease->bindings ; ia != NULL ; ia = ia->next) {
  *insert_ia =
dhc6_dup_ia(ia, file, line);
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&copy->addrs; for (addr = ia->addrs ; addr != NULL ; addr = addr->next) {
  *insert_addr =
dhc6_dup_addr(addr, file, line);
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[91/02/13 12:41:21 mrt] * * Revision 2.5 90/11/05 14:37:33 rpd * Added malloc_fork* code. *
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BUNDLE NOT IN FRAMEWORK;  public static String BUNDLE_REFRESH_FAILURE;  public static String
OSGI_SYSTEMBUNDLE_DESCRIPTION

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Copyright(Element parent)
	NodeList list = getChildrenByName(parent, "copyright"); //$NON-NLS-1$
	if (list.getLength() > 0) {
		if (!Monitor.isCanceled())
			return;
	Element element = (Element)

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Copyright(Element parent) NodeList list = getChildrenByName(parent, "copyright"); //$NON-NLS-1$
	if (list.getLength() > 0) {
		if (!Monitor.isCanceled())
			return;
	Element element = (Element)

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 if (fStoredEvents.size() == 0 && actual != null) br

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case 0x10: diga->status[0] |= IEC958_AES0_CON_EMPHASIS_NONE; break;
case 0x00: diga->status[0] |= IEC958_AES0_CON_EMPHASIS_5015; break;
} if (!(bits & 0x80))
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 b_start_440#else if defined(CONFIG_BOOT_PCI) && defined(CONFIG_MIP405)
 b_start_440#else if defined(CONFIG_BOOT_PCI) && defined(CONFIG_MIP405)
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(protocol) */
  BPF_STMT(BPF_STMT(BPF_LD|BPF_B|BPF_ABS,
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Copyright (c) 2005-2007 Yura Pakhuchiy
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   ntfs_log_info("%s\n%s\n", ntfs_gpl, ntfs_bugs, ntfs_home);}]
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   ntfs_log_info("%s\n%s\n", ntfs_gpl, ntfs_bugs, ntfs_home);}]
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Copyright (c) 2014 Jean-Pierre Andre * * A set of shared functions for ntfs utilities
Copyright (C) 1992-1993 Remy Card <card@masi.ibp.fr>
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   "Allocate and initialize a base or an extent mft "
   "record. If a base mft record\nis not specified, a "
   "base mft record is allocated and initia
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   EXEC_NAME, VERSION);
   ntfs_log_info("%s\n%s\n", ntfs_gpl, ntfs_bugs, ntfs_home); exit(0);}
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Copyright (C) 1995 Theodore Ts * * On error return -1 with errno set to the error code.
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   Usage: %s [-o option[...]] <device|image_file>
   <mount_point>\n"
   Options: ro (read-only mount), windows_names, uid=, gid=, \n"
   umask=, fma
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Copyright (c) 2005-2009 Szabolcs Szakacsits
(C) AC_LINK_IFELSE( AC_LANG_POP) if test x$lt_cv_cc_needs_belf != x"yes" then 
# this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf
CFLAGS="$SAVE_CFLAGS" fi

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Example: ntfs-3g probe --readwrite /dev/sda1
Usage: %s

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Example: ntfs-3g probe --readwrite /dev/sda1

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and plays the committed transactions in order to restore the integrity of metadata
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** This utility will allocate and initialize an mft record.

copyright();

fprintf(stderr, "[attr-name]]\n"
" If attr-type is not specified, 0x80 (i.e. $DATA) "
" is assumed.\n"
" If attr-name is not specified, an unnamed "
" attribut

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** This utility will resize an NTFS volume without data loss.

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import path
from sqlalchemy import create_engine
from werkzeug.wrappers import Request
from werkzeug.utils import cached_property, import_string
from werkzeug.wsgi import ThreadedRequest
from werkzeug.serving importrun_simple
from werkzeug.debug.shared u
from werkzeug.debug import werkzeug debugger

from __future__ importdivision
import os
import gc
import sys
import re
import json
import time
import random
import code
from types import CodeType
from __future__ importwith_statement
from werkzeug.debug import WerkzeugDebugger
from werkzeug.serving import run_simple
from werkzeug.utils import cached_property, import_string
from werkzeug.wsgi import ThreadedRequest
from werkzeug.serving import run_simple
from werkzeug.debug.shared u
from werkzeug.debug import werkzeug debugger

import sys
import os
import errno
import time
import random
import re
import codecs
from traceback import format_exception_only
import pythondatetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
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datetime import datetime
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datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime
datetime import datetime

difflib
creoleparser
from os import import path
from genshi import Stream
from genshi.template import
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.. import sys

import re

import codecs
from traceback import format_exception_only
try:

import sys

import os

import errno

import time

import random

import re

import codecs

from traceback import format_exception_only
try:

import sys

import os

import errno

import time

import random

import re

import codecs

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lcd_printf("MPC823 CPU at %s MHz\n",
strmhz(temp, gd->cpu_clk));

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<malloc.h> #include <pci.h> #include <dm/device-internal.h> #include <dm/lists.h> #include */#ifndef
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#include <errno.h> #include <netdev.h> #include <fsl_ifc.h> #include <fsl_ddr.h> #include
#include <asm/io.h> #include <fdt_su
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<common.h>#include <errno.h>#include <fastboot.h>#include <malloch.h>#include
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(copyright abandoned. Code was * subsequently included in the kernel, thus was re-licensed under the * GNU GPL v2. * * Oct 12, 2000 Matt Domsch <Matt_Domsch@dell.com> * Same crc32 function was us
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<fpm_eth.h>/*#include <asm/io.h>/*#include <asm/immap_85xx.h>/*#include <asm/fsl_serdes.h>
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the pca953x.c driver
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<config.h> #include <errno.h> #include <asm/system.h> #include <asm/types.h> #include

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<asm/arch/soc.h> #ifdef
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"pblimage.h" #include "pbl_crc32.h"
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common/davinci_nand.h> */
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```
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Copyright (c) 2016, NVIDIA CORPORATION. * * Portions based on U-Boot’s rti8169.c.
Copyright (c) 2018 Heinrich Schuchardt <xypron.glpk@gmx.de> * * This unit test checks the following protocol services:
Copyright 2010 Freescale Semiconductor, Inc. */#ifndef __PIXIS_H_ #define __PIXIS_H_ 1
Copyright (c) 1996 - 1999 Russell King. * * This program is free software; you can redistribute it and/or modify * it under the terms of the GNU General Public License version 2 as * published by th
(C) Copyright 2015  DENX Software Engineering GmbH * Written-by: Albert ARIBAUD
<albert.aribaud@3adev.fr> * * Derived from omapimage.c: *
Copyright: Monta Vista Software, Inc. */
Copyright (c) 1992 Simon Glass */
(C) Copyright 2004, Li-Pro.Net <www.li-pro.net> * Stephan Linz <linz@li-pro.net> */
Copyright (C) 2011 * Stefano Babic, DENX Software Engineering, sbabic@denx.de. *
Copyright (C) 2010 Ilya Yanok, Emcraft Systems, yanok@emcraft.com * * This file is mostly identical to the original from * board/freescale/mpc8308rdb/sdram.c
(C) Copyright 2003 * Wolfgang Denk, DENX Software Engineering, <wd@denx.de> */
(C) Copyright 2015 * Heiko Schocher, DENX Software Engineering, hs@denx.de. * * Based on:
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3d85836ee13774d45531928361809612aa0a18db *
Copyright (C) 2016 Paul Kociatkowski <contact@paulk.fr> */
Copyright (C) 2003 Bernardo Innocenti <bernie@develer.com> * Based on former asm-ppc/div64.h and
asm-m68knommu/div64.h * * Optimization for constant divisors on 32-bit machines:
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Copyright (C) 2013,2014 - ARM Ltd * Author: Marc Zyngier <marc.zyngier@arm.com> * * This program is free software; you can redistribute it and/or modify * it under the terms of the GNU General Pub
(C) Copyright 2014 Topic Embedded Products * * Configuration for Zynq Evaluation and Development Board - Miami * See zynq-common.h for Zynq common configs
Copyright 2014 Freescale Semiconductor, Inc. * * Configuration settings for the Freescale i.MX6SX Sabreautoboard.
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Copyright (C) 2009 - 2012 Paul Mundt
Copyright 2015 Freescale Semiconductor, Inc. *
Copyright (C) 2011-2013 Marek Vasut <marex@denx.de> */#include <common.h>#include <mallocl.h>#include <video_fb.h>
Copyright (c) 2001 Navin Boppuri / Prashant Patel * <nboppuri@trinetcommunication.com>, *
<pmpatel@trinetcommunication.com>
(C) Copyright 2007-2012 * Allwinner Technology Co., Ltd. <www.allwinnertech.com> * Tom Cubie<brangliang@allwinnertech.com> *
Copyright (c) 2001 Charles Mott <cm@linktel.net>
Copyright (c) 2016 Rockchip Electronics Co., Ltd */#include <asm/io.h>#include <asm/arch/hardware.h>
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Copyright 2015 - 2016 Xilinx, Inc. * * Michal Simek <michal.simek@xilinx.com>
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Embedded code to load Lattice’s FPGA: *
(C) Copyright 2012 Renesas Solutions Corp. */#include <common.h>#include <asm/io.h>
Copyright (C) 2015 Renesas Electronics Europe GmbH

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(C) Copyright 2013 * Bo Shen <voice.shen@atmel.com> */
(C) Copyright 2014 * Texas Instruments Incorporated * Refactored common functions in to gpimage-
common.c. *
Copyright (C) 2012 Atmel Corporation */#ifndef __ATMEL_HLCDC_H__#define __ATMEL_HLCDC_H__
Copyright (C) 2012 Vikram Narayanan * <vikram186@gmail.com>
(C) Copyright 2001 * Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI (ITALY), arsenio@tin.it */
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Copyright (c) 2000 MontaVista Software, Inc. Dan Malek (dmalek@jlc.net) *
(C) Copyright 2004 * ARM Ltd. * Philippe Robin, <philippe.robin@arm.com> * Configuration for Compact Integrator board. */
(C) Copyright 1996-2005 Paul Mackerras. * * Adapted for 64bit PowerPC by Dave Engebretsen and Peter Bergner. * (engebret|bernger)@us.ibm.com
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Vikas Sajjan <vikas.sajjan@samsung.com> */
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<inkidae@samsung.com> * Minkyu Kang <mk7.kang@samsung.com> */
Copyright (C) 2013 Imagination Technologies * Author: Paul Burton <paul.burton@mips.com> * * Setup code for the FDC37MB17 super I/O controller
(C) Copyright 2003 * Kai-Uwe Bloem, Auerswald GmbH & Co KG, <linux-development@auerswald.de> */
(C) Copyright (C) 2013 Samsung Electronics Co.Ltd * Authors: Vivek Gautam <gautam.vivek@samsung.com> *
Vikas Sajjan <vikas.sajjan@samsung.com> */
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Copyright (c) 2016 Google, Inc * * From coreboot src/soc/intel/broadwell/sata.c
Copyright (c) 2016 Google, Inc */#ifndef _ASM_ARCH_SDRAM_AST2500_H#define _ASM_ARCH_SDRAM_AST2500_H
Copyright (C) 2017 Masahiro Yamada <yamada.masahiro@socionext.com> */
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(C) Copyright 2007 Eurotech S.P.A. <info@eurotech.it> */
(C) Copyright (C) 2014, Gumstix, Incorporated - http://www.gumstix.com/ * Based on board/ti/am335x/board.c from Texas Instruments, Inc. */
Copyright (c) 2017 Google, Inc * * This file follows drivers/of/base.c with functions in the same order as the * Linux version.
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copyright : * * ------------------------------------------------------------- * * 
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<mgroeger@sysyo.com> */
Copyright (C) 2004 David Brownell * * Ported to U-Boot by: Thomas Smits <ts.smits@gmail.com> and * 
Remy Bohmer <linux@bohmer.net>
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2002-2006 Thomas Gleixner 
(tglx@linutronix.de) * * Credits:
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from mtd-utils *
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(C) Copyright 2013 Oliver Schinagl <oliver@schinagl.nl> * http://linux-sunxi.org * * Based on sun6i 
sources and earlier U-Boot Allwiner A10 SPL work
Copyright (C) 2004 by David Brownell * * updates to merge with Linux 2.6, better match RNDIS spec */
Copyright (C) 2014, NVIDIA
Copyright Altera Corporation (C) 2014-2015 */#ifndef _SDRAM_H_#define _SDRAM_H_
<ash@gumstix.com> * * Configuration settings for the Gumstix DuoVero board. * See omap4_common.h for OMAP4 c
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(C) Copyright 2003 * Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net */#include 
<common.h>#include <malloc.h>#include <mmc.h>#include <linux/errno.h>#include
<asm/io.h>#include <a
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(C) Copyright 2007 * Michael Schwingen, <michael@schwingen.org> * * based in great part on
jedec_probe.c from linux kernel:
Copyright (C) 1997 Sten Wang
Copyright (C) 2008 Atmel Corporation */
Copyright 2016 3ADEV <http://3adev.com> * Written-by: Albert ARIBAUD <albert.aribaud@3adev.fr> * *
Configuration settings for the phytec PCM-052 SoM-based BK4R1.
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by: Sylvain Lemieux <slemieux@tycoint.com> */
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Copyright (C) 2001 * Murray Jensen <Murray.Jensen@cmst.csiro.au> */
Copyright 2010 * Matt Wadel, <matt.wadel@linaro.org> */#ifdef _WDT_H_#define _WDT_H_
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Copyright (C) 2007 * Sascha Hauer, Pengutronix *
Copyright (C) 2016 * Author: Amit Singh Tomar, amitominator25@gmail.com * * Ethernet driver for
H3/A64/A83T based Soc's
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Original Copyrights follow: *
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Copyright Altera Corporation (C) 2014-2015 */#include <common.h>#include <errno.h>#include
<asm/arch/fpga_manager.h>#include <asm/arch/32bit.h>#include <asm/arch/sdram.h>#include
<asm/arch/32bit.h>#include <watchdog.h>#include <asm/arch/fpga_manager.h>#include
<asm/arch/sdram.h>#include <asm/arch/32bit.h>#include<
(C) Copyright 2008 Atmel Corporation */#include <common.h>#include <dm.h>#include
<asm/arch/sdram.h>#include <asm/arch/sdram.h>#include <asm/arch/sdram.h>
<asm/arch/32bit.h>#include <asm/arch/32bit.h>#include <asm/arch/32bit.h>
<asm/arch/32bit.h>#include <asm/arch/32bit.h>
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Copyright (C) 2018# Lukasz Majewski, DENX Software Engineering, lukma@denx.de# This is an example file to generate boot.scr - a boot script for U-Boot# Generate boot.scr:
Copyright 2007-2014 Freescale Semiconductor, Inc. */
(C) Copyright 2016 Broadcom Ltd. */#include <common.h>#include <asm/system.h>#include <asm/armv8/mmu.h>
(C) Copyright 2014 * NVIDIA Corporation <www.nvidia.com> *
(c) Copyright 2016, Data61 * Commonwealth Scientific and Industrial Research Organisation (CSIRO) **
Based on jetson-tk1.h which is:
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Copyright (c) 2009 Wind River Systems, Inc. * Tom Rix <Tom.Rix@windriver.com> */
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Copyright (C) 2015-2018, Bin Meng <bmeng.cn@gmail.com>
(C) Copyright 2014 CompuLab, Ltd. <www.compulab.co.il> ** Authors: Igor Grinberg
<grinberg@compulab.co.il>
Copyright (C) 1985 MIPS Computer Systems, Inc.
(C) Copyright 2004-2010 * Texas Instruments, <www.ti.com> */#ifndef _OMAP2PLUS_I2C_H_#define _OMAP2PLUS_I2C_H_
(C) Copyright 2013 * David Feng <fenghua@phytium.com.cn> * Sharma Bhupe
<bhupe.sharma@freescale.com> */#include <common.h>#include <dm.h>#include <malloc.h>#include <errno.h>#include <netd
(C) Copyright 2010-2011 * Graeme Russ, <graeme.russ@gmail.com> */
Copyright 2012-2016 Freescale Semiconductor, Inc. */
Copyright 2014 Freescale Semiconductor, Inc. */#include <config.h>#include <common.h>#include <dm.h>#include <phy.h>
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(C) Copyright 2009-2015 * Daniel Gorsulowski <daniel.gorsulowski@esd.eu> * esd electronic system
design gmbh <www.esd.eu> ** Configuration settings for the esd MEESC board.
Copyright (C) 2010, 2011 Nobuhiro Iwamatsu <iwamatsu@nigauri.org> */
Copyright (C) 2012, Stefano Babic <sbabic@denx.de> ** Based on flea3.c and mx35pdk.c
Copyright (c) 2001 Gerd Mennchen <Gerd.Mennchen@icn.siemens.de>
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Linux.
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support which is
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Copyright (C) 2006-2009 Freescale Semiconductor, Inc. ** Based on MPC8349 PCI support but w/o PIB
related code.
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Copyright (C) 2016 Masahiro Yamada <yamada.masahiro@socionext.com> */
Copyright (c) 2014 The Chromium OS Authors. */
<common.h>#include <errno.h>#include <dm.h>#include <malloc.h>#include <asm/arch/imx-regs.h>#include

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Implementation:
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(C) Copyright 2009-2014 * Gerald Kerma <dreagle@doukki.net> * Marvell Semiconductor
<www.marvell.com> * Written-by: Siddarth Gore <gores@marvell.com> */
Copyright 2010-2011 Freescale Semiconductor * Author: Timur Tabi <timur@freescale.com> */ * This file
provides support for the ngPIXIS, a board-specific FPGA used on * some Freescale reference boa-
(C) Copyright David Brownell 2000 (kernel hotplug, usb_device_id)
Copyright (C) 2006 Nokia Corporation * Author: Imre Deak <imre.deak@nokia.com> */ * This program is
free software; you can redistribute it and/or modify * it under the terms of the GNU General Publ
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Copyright (c) 2015 Samsung Electronics * * USB3 (DWC3) PHY uboot init
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Copyright (C) 2017 * * Derived from linux/drivers/tty/serial/bcm63xx_uart.c:
Copyright (C) 2010 Eric C. Cooper <ecc@cmu.edu> */ * Based on sheevaplug.h originally written by *
Prafulla Wadaskar <prafulla@marvell.com>
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Copyright (C) 2013 Atmel Corporation * Bo Shen <voice.shen@atmel.com> *
Copyright (c) 2014-2015, NVIDIA CORPORATION. All rights reserved
Copyright (C) 2007 Atmel Corporation */#include <common.h>#include <clk.h>#include <dm.h>#include
<fdtdec.h>#include <spi.h>#include <malloc.h>#include <wait_bit.h>
Copyright 2016 NXP Semiconductor. * Author: Wang Dongsheng <dongsheng.wang@freescale.com> */
(C) Copyright 2008-2014 Rockchip Electronics * Peter, Software Engineering, <superpeter.cai@gmail.com>.
*/#ifndef _ASM_ARCH_CRU_RK3288_H#define _ASM_ARCH_CRU_RK3288_H
Copyright (C) 2015-2017 Socionext Inc. * Author: Masahiro Yamada <yamada.masahiro@socionext.com> */
(C) Copyright 2000 * Wolfgang Denk, DENX Software Engineering, wd@denx.de. * * Add to readline
cmdline-editing by
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Copyright (C) 2015 Free Electrons * Gregory CLEMENT <gregory.clement@free-electrons.com> */
Copyright (c) 2018 Heinrich Schuchardt <xypron.glpk@gmx.de> */ * This test checks the StartImage boot
service. * The efi_selftest_miniapp_exit.efi application is loaded into memory * and started.
(C) Copyright 2009 * Heiko Schocher, DENX Software Engineering, hs@denx.de. * Changes for multibus/multiaadapter I2C support. *
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Copyright (C) 2006-2008 David Brownell * U-Boot porting: Lukasz Majewski <l.majewski@samsung.com>
*/#undef DEBUG
Copyright (c) 2017 Imagination Technologies Ltd. * * Relocation data, found in the .rel section, is generated
by the mips-relocs * tool & contains a record of all locations in the U-Boot binary tha
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#include <asm/arch/mmc_host_def.h>
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zsh 5.6.2 debian:zsh/5.6.2-3
(C) (wc_data(C) >> 2)#define WCB_IF(T,O) wc_bld(WC_IF, ((T) | ((O) << 2)))
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(C) wc_data(C)#define WCB_TRY(O) wc_bld(WC_TRY, (O))
(C) wc_data(C)#define WCB_REPEAT(O) wc_bld(WC_REPEAT, (O))
copyright information.None of the core functions are affected by this, so those files may simply be omitted.
(C) (wc_data(C)#define WC_FOR_PPARAM 0#define WC_FOR_LIST 1#define WC_FOR_COND 2
(C) (wc_data(C)#define WC_WHILE_WHILE 0#define WC_WHILE_UNTIL 1
(C) (wc_data(C)#define WCB_SIMPLE(N) wc_bld(WC_SIMPLE, (N))
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(C) (wc_data(C)#define WC_ASSIGN_SCALAR 0#define WC_ASSIGN_ARRAY 1#define WC_ASSIGN_NEW 0/* * In normal assignment, this indicate += to append. * In assignment following a
typeset 
(C) (wc_data(C)#define WC_CASE_FREE) #define WCB_CASE(T,O) wc_bld(WC_CASE, ((T) | ((O) <<
WC_CASE_FREE)))
(C) ((C) >> WC_CODEBITS)#define wc_bdata(D) (((wordcode) (D)) << WC_CODEBITS)
(C) ((int)(wc_data(C)#define WCB_REDIR(T) wc_bld(WC_REDIR, (T))/* Size of redir is 4 words if
REDIR_VARID_MASK is set, else 3 */#define WC_REDIR_WORDS(C) \n(C) (wc_data(C)#define WC_SUBLIST_END 0#define WC_SUBLIST_AND 1#define WC_SUBLIST_OR 2
(C) (wc_data(C)#define WC_CASE_HEAD 0#define WC_CASE_FREE) /* Next bit available in integer
(C) (wc_data(C) >> 1)#define WCB_SELECT(T,O) wc_bld(WC_SELECT, ((T) | ((O) << 1)))
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(C) (wc_data(C)#define WC_PIPEPIPE(1)#define WC_PIPE_PIPE, (T) | (L) << 1)))
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