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MX-NOS v3.x

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(uptime 3.0.1)

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(Net-SNMP 5.3.1)

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(Click - Python Command Line Utility 7.0)

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(tcpdump 4.9.2)

<torsten@debian.org> and Romain Francoise <rfrancoise@debian.org>.

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(pycparser 2.19)

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(python-cryptography 2.3.1)

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(Shadow Tool Suite 4.1.4.2+svn3283)

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(python-werkzeug 0.12.2)

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(libpcap 1.9.0)

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(OpenSSH 7.4p1)

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(EventLog Library 0.2.12)

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(Jinja 2.10)

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(itsdangerous 0.24)

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(iputils s20180629)

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(Flask 0.12.2)

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(sg3\_utils 1.44)

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(libevent - an event notification library 2.1.8-stable)

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(psutil 5.5.0)

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(SoftEtherVPN 5.01.9671)

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(libgpg-error 1.33)

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(busybox 1\_29\_3, e2fsprogs 1.44.5, Linux Kernel 4.4.120)

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supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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b) Use a suitable shared library mechanism for linking with the Library. A

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(libjwt 1.9.0, libtalloc 2.1.8)

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Version 3, 29 June 2007

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Version 2, June 1991

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However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing

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- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
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(pypi/setuptools 40.8.0)

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(mingw-expat 2.2.7)

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(netifaces 0.10.6)

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(pip 19.0.3)

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(jansson 2.12)

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(Python six 1.12.0)

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(color-string 0.6.0)

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(Qix-/color-convert 1.9.3)

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(python-jsonschema 2.5.1)

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(parse5 5.1.1)

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(asn1crypto 0.24.0)

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(libxml2 2.9.9)

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(moment/moment 2.24.0)

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(card10-sys 1.9.0)

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(zsh 5.6.2)

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(parson 20180104-snapshot-c147744b)

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(libffi 3.3-rc0)

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(python-cffi 1.11.2)

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(@angular/cdk 8.2.3, @angular/material-moment-adapter 8.2.3, Material Design for Angular 8.2.3)

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(@angular/flex-layout 9.0.0-beta.31)



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(xmlbuilder-js v11.0.1)

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(@circlon/angular-tree-component 9.0.5)

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(hammer.js 2.0.8)

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(ricmoo/pyaes 20170920-snapshot-23a1b4c0)

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(color-name 1.1.3, color-name 1.1.4)

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(PyJWT 1.4.2)

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(mobx 4.14.1)

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write back conf file */                writeflag = 1;                break;                case 'h':
/* print help */                usage ("");                break;                case ':':

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 log\_perror = tmp; } else  
 log\_fatal("invalid log facility"); data\_string\_forget(&db, MDL); }  
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 &copy->address,  
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 &copy->bindings; for (ia = lease->bindings ; ia != NULL ; ia = ia->next) { \*insert\_ia =  
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copyright);          log_info(arr);          log_info(url);          log_info("%s", "");          }
else {          log_perror = 0;          quiet_interface_discovery = 1;          }
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copyright);          log_info (arr);          log_info (url);    } else {          log_perror = 0;    }
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in alloc.c here. These are very * basic functions, and it is very important that they work co
copyright);          log_info(arr);          log_info(url);    } else          log_perror = 0;
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copyright);                                log_info(arr);                                log_info(url);
                                log_perror = tmp;                                } else
                                log_fatal("invalid log facility");                                data_string_forget(&db, MDL);                                }
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MRNs_name_compress_list and * MRNs_name_uncompress_list. These two functions rely on most of *
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 COPYRIGHT); return 0; break; case 'w': /\*  
 write back conf file \*/ writeflag = 1; break; case 'h':  
 /\* print help \*/ usage (""); break; case ':':  
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</xsl:template>

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```
copyright);          log_info(arr);          log_info(url);          log_info("%s", "");          }
else {          log_perror = 0;          quiet_interface_discovery = 1;          }
```

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```
copyright);          log_info(arr);          log_info(url);          } else          log_perror = 0;
```

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```
copyright);          log_info(arr);          log_info(url);
          log_perror = tmp;          } else
          log_fatal("invalid log facility");          data_string_forget(&db, MDL);          }
```

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 loggingimport collectionsfrom datetime import timedeltafrom six import string\_typesfrom f  
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 requestfrom .core import \*  
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update\_wrapperfrom flask import make\_response, request, current\_appfrom .core import \*  
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 Copyright 2006 The FreeRADIUS server project \*/#ifndef \_RLM\_EAP\_TLS\_H#define \_RLM\_EAP\_TLS\_H  
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 <freeradius-devel/exfile.h>  
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(C)   btowc (C)# endif# define STRLEN(S) strlen (S)# define STRCAT(D, S) strcat (D, S)# define  
MEMPCPY(D, S, N) \_\_mempcpy (D, S, N)# define MEMCHR(S, C, N) memchr (S, C, N)# define STRCOLL(S1,  
S2)

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'./add.js';export { default as after } from './after.js';export { default as ary } from './ary

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'./array.js';import collection from './collection.js';import date from './date.js';import func from './fun

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__CRC_H__ #define __CRC_H__
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    elif copyright_str is not header:
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*****/#include
<stdio.h>#include <string.h>#include <stdlib.h>#include "Nano100Series.h"

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 (C) DBUG\_EXECUTE("qcache", (C)->cache\_dump(); (C)->queries\_dump();(C)-  
 >tables\_dump();})#else#define MUTEX\_LOCK(M) pthread\_mutex\_lock(M)#define MUTEX\_UNLOCK(M)  
 pthread\_mutex\_unlock(M)#define RW\_W  
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 welcome in the documentation of your product. If for some reason you cannot include such an ac  
 Copyright ♦ Sleepycat Software Inc. 1997-2002\0" VALUE "OriginalFilename", "libdb.dll\0"  
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 (C)#define thread\_safe\_sub(V,C,L) (V)-=(C)#define statistic\_increment(V,L) (V)++  
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 ---\n"); client\_test\_print\_version(); fprintf(stdout, "Usage: %s [OPTIONS]\n\n", my  
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 (C) AC\_TRY\_LINK( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is  
 probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;AC  
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copyright> \* \* \$Id: GenerateEcore2XMLActionDelegate.java,v 1.3.2.1 2005/08/02 14:54:51 khussey Exp \*/package org.eclipse.emf.mapping.ecore2xml.action;

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copyright> \* \* \$Id: XSDConstraint.java,v 1.2 2005/06/08 06:23:01 nickb Exp \*/package org.eclipse.xsd;

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org.eclipse.emf.mapping.ecore2ecore;
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org.eclipse.xsd.impl;
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org.xml.sax;
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```
COPYRIGHT : stateStack.pop(); text = ""; //$NON-
NLS-1$ while (objectStack.peek() instanceof String) {
text = (String) objectStack.pop() + text; }
if (objectStack.peek()
```

```
copyright> * * $Id: Notification.java,v 1.2 2005/06/08 06:19:08 nickb Exp */package
org.eclipse.emf.common.notify;
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copyright> * * $Id: JETBuilder.java,v 1.3 2005/06/08 06:15:56 nickb Exp */package
org.eclipse.emf.codegen.jet;
```

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COPYRIGHT))          bits          if ((diga->status[0] & IEC958_AES0_CON_EMPHASIS)
== IEC958_AES0_CON_EMPHASIS_NONE)          bits |= 0x40;          if (!(diga-
>status[1] & IEC958_AES1_CON_ORIGINAL))          bits |= 0x80;          if ((d
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----- * $Id: pm2fb.h,v 1.26 2000/09/19 00:11:53 illo Exp $
copyright> * * $Id: EMFEditUIPlugin.java,v 1.4 2005/06/08 06:20:52 nickb Exp */package
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org.eclipse.emf.codegen.ecore.genmodel;
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sys.setdefaultencoding() so that users cannot change the # encoding after initialization. The te
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*/package org.eclipse.emf.mapping.ecore2xml.presentation;

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                IFormPage page = editor.findPage(InfoFormPage.PAGE_ID);
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        }
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org.eclipse.emf.codegen.jmerge;
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 <stdarg.h>#include <fcntl.h>#include <sys/stat.h>#include <unistd.h>#include <errno.h>#include  
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org.eclipse.xsd.impl.type;

Copyright office. \*/#include <netinet/in.h>#include <clBufferApi.h>#include <clRmdApi.h>#include <clEoApi.h>#include <string.h>#include <csa114CompSP\_EOServer.h>#include <csa114CompSP\_EOClient

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 COPYRIGHT : case STATE\_LICENSE :  
 handleFeatureState(localName, attributes); break;  
 case STATE\_URL :  
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 org.eclipse.emf.ecore.util;

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```

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copyright> * * $Id: IdentityCommand.java,v 1.2 2005/06/08 05:44:08 nickb Exp */package
org.eclipse.emf.common.command;
```

```
Copyright Text</em> * <!-- begin-user-doc --> * <!-- end-user-doc --> * @see #getCopyrightText()
* @generated * @ordered */ protected static final String COPYRIGHT_TEXT_EDEFAULT =
```

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copyright> * * $Id: XSDSchemaDirectiveImpl.java,v 1.7 2005/06/08 06:23:01 nickb Exp */package
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org.eclipse.emf.common.command;  
&copy", 169); add("&ordf", 170); add("&laquo", 171); add("&not", 172); add("&shy", 173);  
add("&reg", 174); add("&macr", 175); add("&deg", 176); add("  
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ClipboardCopyToClipboardAction\_4=There was a problem when accessing the system clipboard. Retry?  
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 org.eclipse.emf.edit.tree.util;

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 copyright // options.addElement( "-quiet"); options.addElement("-format"); // need this  
 because that's what the handler is using, it's // way easier to process th  
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copy right away    if (value->data[0].v_pointer && !(value->data[1].v_uint &
G_VALUE_NOCOPY_CONTENTS)) node->free (value->data[0].v_pointer);    value->data[1].v_uint =
need_free ? 0 : G_VALUE_
```

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        synchronized (DocumentLineDiffer.this)                                if
(isCanceled(monitor))                                                         return
Status.CANCEL_STATUS;                                                         if
(fStoredEvents.size() == 0 && actual != null)
    br

```

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 IEC958\_AES0\_CON\_EMPHASIS\_NONE; break; case 0x00: diga->status[0] |=  
 IEC958\_AES0\_CON\_EMPHASIS\_5015; break; } if (!(bits & 0x80))  
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}
else if (name.equals(P_IMAGE)) {
    setImageName((String) newValue);
}
else
    super.restoreProperty(name, oldValue, new
```

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org.eclipse.emf.mapping.domain;
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copyright); schema.addDocumentSection(section); StringWriter swriter = new  
 StringWriter(); try { PrintWriter writer = new PrintWriter(swriter, true);  
 schema.save(writer); swriter.close(  
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org.eclipse.emf.importer.ui.contribution;
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```
Copyright status#define AC97_SC_PRE          0x0008      /* Preemphasis status */#define
AC97_SC_CC_MASK          0x07f0      /* Category Code mask */#define AC97_SC_L          0x080
```

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```
Copyright Text</b></em> * <!-- begin-user-doc --> * <!-- end-user-doc --> * @generated *
@ordered */ int GEN_MODEL__COPYRIGHT_TEXT = GEN_BASE_FEATURE_COUNT + 0;
```

Copyright office. \*/#ifndef \_CL\_COMPDEFS\_H# define \_CL\_COMPDEFS\_H\_

```
copyright> * * $Id: CodeGenPlugin.java,v 1.4 2005/06/08 06:15:57 nickb Exp */package
org.eclipse.emf.codegen;
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copyright> * * $Id: XSDMaxExclusiveFacetImpl.java,v 1.4 2005/06/08 06:23:01 nickb Exp */package
org.eclipse.xsd.impl;
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 COPYRIGHT)) { stateStack.push(new Integer(STATE\_COPYRIGHT));  
 processInfo(attributes); } else if (elementName.equals(LICENSE)) {  
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<= $#in) && ($in[$i] ne " */\n"))          { $i++; }          $i++ if
($in[$i] eq " */\n");
```

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 @öðé' typ, data = client.append(None, None, None, msg\_string.encode( self.assertEqual(typ, 'OK')  
 self.assertEqual(server.response, ('UTF8 (%s)\r\n' % msg\_string).en  
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 copyright>// // <summary>// Precompiled header for standard bootstrapper application.//  
 </summary>//-----  
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 copyright(self): # 'copyright', 'credits', and 'license' should be in builtins

```

self.assertTrue(hasattr(builtins, "copyright"))    self.assertTrue(hasattr(builtins, "credits"))
copyright)    self.py_copyright.pack(side=LEFT, padx=10, pady=10)    self.py_credits =
Button(py_buttons, text='Credits', width=8,                highlightbackground=self.b
COPYRIGHT SIGN '\u2122' # 0xAA -> TRADE MARK SIGN '\xb4' # 0xAB -> ACUTE ACCENT
'\xa8' # 0xAC -> DIAERESIS '\u2260' # 0xAD -> NOT EQUAL TO '\xc6' # 0xAE -> LAT
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©", 4)    # Support > 100 groups.    pat = '|'.join('x(?P<a%d>%x)y' % (i, i) for i in range(1, 200 +
1))    pat = '(?:%s)(?(200)z|t)' % pat    self.assertEqual(re.match(pat, 'xc8y
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 PyUnicode\_FromString(Py\_GetPlatform())); SET\_SYS\_FROM\_STRING("maxsize",  
 PyLong\_FromSsize\_t(PY\_SSI  
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 loggingimport osimport shutilimport subprocessimport sysimport sysconfigimport types



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 copyright a global variable. ;-) Oh well. \* \* At least one applet (cmp) returns a value different from the typical \* EXIT\_FAILURE values (1) when an error occurs. So, make it configurable \* by  
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 (C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is  
 probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;  
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 CURL\_VERSION\_MAJOR LIBCURL\_VERSION\_MAJOR#define CURL\_VERSION\_MINOR  
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CURL\_VERSION\_PATCH LIBCURL\_VERSION\_P

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```
copyright: {
    dnode_t#ifndef DICT_NODEBUG    rightnode->left = NULL;#endif
    dict_load_next(&load, rightnode, rightnode->key);    rightnode = next;    continue;    }
```

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    "    If attr-type is not specified, 0x80 (i.e. $DATA) "    "is assumed.\n"
    "    If attr-name is not specified, an unnamed "
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 Copyright (C) 2006-2009 Szabolcs Szakacsits\n"  
 Copyright (c) 2005-2007 Yura Pakhuchiy  
 Copyright (c) 2014 Eric Biggers  
 Copyright (c) 2010 Erik Larsson  
 Copyright (c) 2000-2002 Anton Altaparmakov and others  
 Copyright (c) 2002-2006 Szabolcs Szakacsits\n");  
 Copyright (c) 2003-2005 Anton Altaparmakov  
 Copyright (c) 2003 Richard Russon \* \* This function will locate the owner of any given sector or cluster range.  
 Copyright (c) 2006 Hil Liao\n");  
 Copyright (c) 2007-2008 Szabolcs Szakacsits  
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 Copyright (c) 2010-2011 Jean-Pierre Andre  
 Copyright (c) 2008-2009 Jean-Pierre Andre  
 Copyright (c) 2010 Jean-Pierre Andre  
 Copyright (c) 2014 Jean-Pierre Andre\n"); ntfs\_log\_info("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs, ntfs\_home);}  
 Copyright (c) 2002-2003 Richard Russon  
 Copyright (c) 2005-2006 Szabolcs Szakacsits\n"); ntfs\_log\_info("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs, ntfs\_home);}  
 Copyright (c) 2005-2007 Anton Altaparmakov  
 Copyright (c) 2014 Jean-Pierre Andre \* \* A set of shared functions for ntfs utilities  
 Copyright (C) 1992-1993 Remy Card <card@masi.ibp.fr>  
 Copyright (c) 2004-2005 Anton Altaparmakov\n" "Allocate and initialize a base or an extent mft " "record. If a base mft record\nis not specified, a " "base mft record is allocated and initia  
 Copyright (c) 2007-2010 Jean-Pierre Andre  
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 Copyright (c) 2007-2016 Jean-Pierre Andre \* \* Options :  
 Copyright (C) 2003-2013 Free Software Foundation, Inc.  
 Copyright (c) 2011-2015 Jean-Pierre Andre\n\n", EXEC\_NAME, VERSION);  
 ntfs\_log\_info("%s\n%s%s", ntfs\_gpl, ntfs\_bugs, ntfs\_home); exit(0);}  
 Copyright (c) 2000-2005 Anton Altaparmakov  
 copyright - print copyright statements \*/  
 Copyright (c) 2008 Jean-Pierre Andre \*  
 Copyright (c) 2002-2006 Anton Altaparmakov  
 Copyright (c) 2006 Yuval Fledel \* \* This utility will check and fix errors on an NTFS volume.  
 Copyright (C) 2004-2013 Free Software Foundation, Inc.  
 Copyright (c) 2007 Yura Pakhuchiy  
 Copyright (c) 2004-2008 Szabolcs Szakacsits  
 Copyright (C) 1995 Theodore Ts \* \* On error return -1 with errno set to the error code.  
 copyright-format/1.0/Upstream-Name: NTFS-3G AdvancedUpstream-Contact:  
<http://www.tuxera.com/forum/Source: http://www.tuxera.com/community/ntfs-3g-advanced/>  
 Copyright: 2000-2014 ntfs-3g Development Team  
 Copyright (c) 2001-2004 Anton Altaparmakov  
 Copyright (C) 2002-2013 Free Software Foundation, Inc.  
 Copyright (c) 2008-2010 Jean-Pierre Andre  
 Copyright (c) 2004-2013 Anton Altaparmakov  
 Copyright (C) 2009 Erik Larsson\n""\n""Usage: %s [-o option[,...]] <device|image\_file>  
 <mount\_point>\n""\n""Options: ro (read-only mount), windows\_names, uid=, gid=,\n" " umask=,  
 fma  
 Copyright (c) 2004-2005 Holger Ohmacht\n"

Copyright (c) 2014-2015 Jean-Pierre Andre\n"); ntfs\_log\_info("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs, ntfs\_home);}

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Copyright (c) 2003-2005 Richard Russon\n");

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Copyright (c) 2005-2006 Szabolcs Szakacsits \* \* This utility will locate the owner of any given sector or cluster.

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Copyright (c) 2002 Anton Altaparmakov

Copyright (c) 2000-2013 Anton Altaparmakov

Copyright (c) 2003 Lode Leroy\n");

Copyright (c) 2007-2016 Jean-Pierre Andre \* \* A quick'n dirty program scanning owners of files in \* "c:\Documents and Settings" (and "c:\Users") \* and asking user to map them to Linux

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(C) AC\_LINK\_IFELSE( AC\_LANG\_POP]) if test x"\$lt\_cv\_cc\_needs\_belf" != x"yes"; then # this is probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf CFLAGS="\$SAVE\_CFLAGS" fi ;;

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Copyright (C) 1998-2005 Roman Hodek <Roman.Hodek@informatik.uni-erlangen.de>

Copyright (c) 2005-2006 Yura Pakhuchiy

Copyright (c) 2012-2016 Jean-Pierre Andre\n"); printf("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs, ntfs\_home);}

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Copyright (c) 2004 Carmelo Kintana

Copyright (c) 2004 Carmelo Kintana\n");

Copyright (C) 2007 Szabolcs Szakacsits\n""\n""Usage: %s <--readonly|--readwrite> <device|image\_file>\n""\n""Example: ntfs-3g.probe --readwrite /dev/sda1\n""\n""%s";

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Copyright (C) 2004 Free Software Foundation, Inc.# Written by Scott James Remnant, 2004

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 Copyright (c) 2003-2005 Anton Altaparmakov\n");  
 Copyright (c) 2013-2014 Jean-Pierre Andre \* \* This utility will recover deleted files from an NTFS volume.  
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 Copyright (c) 2015 Jean-Pierre Andre \*  
 Copyright (c) 2002-2003 Richard Russon\n");  
 Copyright (C) 1996, 1997, 1998, 1999, 2000, 2001, 2003, 2004, 2005, # 2006, 2007, 2008,  
 2009, 2010, 2011 Free Software# Foundation, Inc.# Written by Gordon Matzigke  
 Copyright (C) 1994-1996, 1999-2002, 2004-2013 Free Software Foundation, Inc.  
 Copyright (c) 2002-2005 Anton Altaparmakov \* \* This utility will truncate a specified attribute belonging to  
 a \* specified inode, i.e. file or directory, to a specified length.  
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 Copyright (c) 2016 Jean-Pierre Andre  
 Copyright (c) 2002-2008 Szabolcs Szakacsits  
 Copyright (c) 2009 Erik Larsson \* \* This file is originated from the Linux-NTFS project.  
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 Copyright (C) 2008-2014 Daniel Baumann <mail@daniel-baumann.ch>  
 Copyright (c) 2002-2005 Richard Russon  
 Copyright (c) 2008-2016 Jean-Pierre Andre  
 Copyright (c)\n"); printf(" 2002-2004 Matthew J. Fanto\n"); printf(" 2002-2006 Anton  
 Altaparmakov\n"); printf(" 2002-2005 Richard Russon\n"); printf(" 2003-2006 Szabolcs  
 Szakacsits\n")  
 Copyright (c) 2003 Richard Russon\n");  
 Copyright (c) 2007 Jean-Pierre Andre  
 Copyright (c) 2011-2015 Jean-Pierre Andre \* \* This utility will dump a file's attributes.  
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 ntfs\_home);}  
 Copyright (c) 2004-2005 Richard Russon  
 Copyright (C) year name of author  
 Copyright (c) 2005 Anton Altaparmakov\n"  
 Copyright (c) 2014-2015 Jean-Pierre Andre \* \* This utility will decrypt files and print the decrypted data on  
 the standard \* output.  
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 Copyright (c) 2009 Martin Bene  
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 Copyright (c) 2009 Martin Bene  
 Copyright (c) 2006 Szabolcs Szakacsits  
 Copyright (c) 2000-2005 Anton Altaparmakov \* \* This utility will interpret the contents of the journal  
 (\$LogFile) of an \* NTFS partition and display the results on stdout. Errors will be output to  
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 Copyright (c) 2006-2008 Szabolcs Szakacsits  
 Copyright (C) 1993-1994 David Hudson <dave@humbug.demon.co.uk>  
 Copyright (c) 2002-2005 Anton Altaparmakov \* \* This utility will allocate and initialize an mft record.  
 copyright(); fprintf(stderr, "Usage: "[attr-name]]\n" " If attr-  
 type is not specified, 0x80 (i.e. \$DATA) " "is assumed.\n" " If attr-  
 name is not specified, an unnamed " "attribut  
 Copyright (c) 2011-2016 Jean-Pierre Andre \* \* This utility will resize an NTFS volume without data loss.  
 Copyright (C) 1994 X Consortium  
 Copyright (c) 2000-2004 Anton Altaparmakov  
 Copyright (c) 2007 Yura Pakhuchiy\n");  
 Copyright (c) 2002-2003 Richard Russon \* \* This utility will locate the owner of any given sector or cluster.  
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 Copyright (c) 2004-2006 Anton Altaparmakov\n"  
 copyright(void){  
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 Copyright (c) 2005 Anton Altaparmakov  
 Copyright (c) 2002-2003 Richard Russon\n");  
 Copyright (c)\n"); ntfs\_log\_info(" 2002 Matthew J. Fanto\n"); ntfs\_log\_info(" 2002-2005  
 Anton Altaparmakov\n"); ntfs\_log\_info(" 2002-2003 Richard Russon\n"); ntfs\_log\_info("  
 2012-  
 Copyright (c) 2003-2006 Anton Altaparmakov  
 (C)!b go :more /\./{ N s/\n# / / b more  
 Copyright (c) 2014 Jean-Pierre Andre \* \* This utility will copy file to an NTFS volume.  
 Copyright (c) 2005-2008 Szabolcs Szakacsits  
 Copyright (C) 1996, 1997, 1998, 1999, 2000, 2001, 2003, 2004, 2005, 2006,  
 Copyright (c) 2007 Yura Pakhuchiy \* \* This utility will concatenate files and print on the standard output.  
 Copyright (c) 2002 Anton Altaparmakov  
 Copyright (C) 1998 H. Peter Anvin <hpa@zytor.com>  
 Copyright (c) 2010-2014 Jean-Pierre Andre\n"); ntfs\_log\_info("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs,  
 ntfs\_home);} }  
 Copyright (c) 2000-2007 Anton Altaparmakov\n");  
 Copyright (c) 2006-2007 Szabolcs Szakacsits  
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 Copyright (c) 2014 Jean-Pierre Andre \*  
 Copyright (c) 2004 Yura Pakhuchiy\n"); ntfs\_log\_info("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs,  
 ntfs\_home);} }  
 Copyright (c) 2007-2016 Jean-Pierre Andre\n"); printf("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs,  
 ntfs\_home);} }  
 Copyright (c) 2002-2005 Richard Russon\n"  
 Copyright (c) 2004 Yura Pakhuchiy \* \* This utility will overwrite unused space on an NTFS volume.  
 Copyright (c) 2013-2014 Jean-Pierre Andre\n" "Allocate clusters to a specified  
 attribute of " "a specified file.\n");}  
 Copyright (c) 2013-2014 Jean-Pierre Andre\n"); ntfs\_log\_info("\n%s\n%s%s\n", ntfs\_gpl, ntfs\_bugs,  
 ntfs\_home);} }  
 Copyright (c) 2004 Anton Altaparmakov \* \* A set of shared functions for ntfs utilities  
 Copyright (C) 2015 Andreas Bombe <aeb@debian.org>  
 Copyright (c) 2007-2015 Jean-Pierre Andre  
 Copyright (c) 2005 Anton Altaparmakov\n");  
 copyright(); ntfs\_log\_info("Usage: " -n Do not write to disk\n"  
 " -f Force execution despite errors\n" " -q Quiet execution\n"  
 " -v Verbose execution\n" " -v  
 Copyright (c) 2002-2006 Szabolcs Szakacsits\n"

Copyright (C) 2004, 2005, 2007, 2008, 2009 Free Software Foundation, Inc. Written by Gary V. Vaughan, 2004

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 for more details. """from coolmagic.helpers import \*

copyright: (c) 2014 by Armin Ronacher. :license: BSD, see LICENSE for more details. """from \_\_future\_\_  
 import with\_statement

copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD. """from os  
 import path from sqlalchemy import create\_engine from werkzeug.utils import redirect from werkzeug

copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD, see LICENSE  
 for more details. """from \_\_future\_\_ import with\_statement import gdbm from threading import Lock

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copyright: (c) 2014 by Armin Ronacher. :license: BSD, see LICENSE for more details. """import sys import  
 re import io

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 for more details. """from types import ModuleType import sys

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copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD, see LICENSE  
 for more details. """import os import sys sys.path.append(os.path.join(os.path.dirname(\_\_file\_\_)))

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 tempfile import gettempdir

copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD. """import  
 re from werkzeug.wrappers import BaseRequest, BaseResponse from werkzeug.exceptions import HTTPExc

copyright: (c) 2014 by Armin Ronacher. :license: BSD, see LICENSE for more details. """import os import  
 pytest

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copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD. """from time  
 import asctime from werkzeug.serving import run\_simple from werkzeug.wrappers import BaseRequest

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 for more details. """import re

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 for more details. """import os import redis import urlparse from werkzeug.wrappers import Request

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 for more details. """from os import path, listdir from coolmagic.utils import Request, local\_man

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 for more details. """from datetime import datetime

copyright: (c) 2014 by the Werkzeug Team, see AUTHORS for more details. :license: BSD, see LICENSE  
 for more details. """import re import os import posixpath import mimetypes from itertools import

copyright: (c) 2014 by Armin Ronacher. :license: BSD, see LICENSE for more details. """import pytest

copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD, see LICENSE  
 for more details. """from werkzeug.serving import run\_simple from werkzeug.wrappers import BaseRequest

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 LICENSE for more details. """try:

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 simplewiki.utils import Response, Pagination, generate\_template, href from simplewiki.database import

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 ssl import subprocess import textwrap

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```

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sys import re import codecs from traceback import format_exception_only try:
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for more details. """from coolmagic.utils import Response, TemplateResponse, ThreadedRequest, \
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for more details. """import os from coolmagic import make_app from werkzeug import script
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for more details. """import re
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datetime import datetime, date from plnt.database import Blog, Entry from plnt.utils import Paginati
copyright-format/1.0/Upstream-Name: WerkzeugUpstream-Contact: Armin Ronacher
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pytest import os import random
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shutil import os import re from datetime import datetime, date from subprocess import Popen
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for more details. """import os import re import sys import uuid import json import time import g
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LICENSE for more details. """from werkzeug.serving import run_simple from werkzeug.utils import ca
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for more details. """from werkzeug.utils import cached_property, import_string from werkzeug.wr
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sys import code from types import CodeType
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datetime import datetime from sqlalchemy import Table, Column, Integer, String, DateTime, \ For
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for more details. """import os import hmac import hashlib import posixpath import codecs from st
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copyright: (c) 2014 by the Werkzeug Team, see AUTHORS for more details. :license: BSD, see LICENSE
for more details. """import os import re from werkzeug._compat import text_type, PY2, to_unicode
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webpylike import WebPyApp, View, Response
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for more details. """import re import string import inspect from weakref import WeakKeyDictionary
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for more details. """from functools import update_wrapper from datetime import datetime, timedelta
copyright: (c) 2014 by the Werkzeug Team, see AUTHORS for more details. :license: BSD, see LICENSE
for more details. """import diff libimport re import uuid import posixpath
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`'html'`, `'text'` or `'xhtml'`. Default is
copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD. """from os
import path from sqlalchemy import create_engine from werkzeug.wrappers import Request from werkz
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 \_\_future\_\_ import with\_statementimport sysimport osimport reimport posixpathimport difflib  
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 datetimeimport pytest  
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 for more details. """import osfrom werkzeug import script  
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 for more details. """from warnings import warn  
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 for more details. """import osimport sysimport werkzeugfrom textwrap import wrapfrom werkzeug  
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 for more details. """import timefrom os import pathfrom threading import Threadfrom cupoftee.  
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 for more details. """import timeimport socketfrom math import logfrom datetime import datetim  
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 for more details. """import refrom time import time, gmtimeimport  
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 for more details. """from \_\_future\_\_ import print\_function  
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 refrom os import pathfrom jinja2 import Environment, FileSystemLoaderfrom werkzeug.local import  
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 for more details. """from werkzeug import script  
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 for more details. """import osimport reimport errnoimport tempfileimport platformfrom hashl  
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 sysimport feedparserfrom time import timefrom datetime import datetimefrom werkzeug.utils impo  
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 for more details. """import reimport codecsimport mimetypesfrom copy import deepcopyfrom ite  
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 for more details. """from os.path import dirname, joinfrom jinja import Environment, FileSystem  
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 for more details. """import copyfrom functools import update\_wrapperfrom werkzeug.wsgi import  
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 </div>  
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 sqlalchemy import MetaData, Table, Column, ForeignKey, Boolean, \ Integer, String, DateTimefro  
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 for more details. """import codectry:  
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 strict\_eqfrom werkzeug.datastructures import ResponseCacheControlfrom werkzeug.http impo  
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 for more details. """import sysimport timeimport os.pathtry:  
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 werkzeug.serving import run\_simplefrom werkzeug.wappers import Request, Response  
 copyright: (c) 2009 by the Werkzeug Team, see AUTHORS for more details. :license: BSD. """from difflib  
 import unified\_difffrom simplewiki.utils import Response, generate\_template, parse\_creole,  
 copyright information for the feed. :param rights\_type: the type attribute for the rights element. One of

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"""import reimport osimport tempfilefrom os import pathfrom time import ti
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"""import sysimport mimetypesfrom time import timefrom random import random
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Copyright (C) 2002-4 Bruce Allen <smartmontools-support@lists.sourceforge.net> */#ifndef
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(C) AC_LINK_IFELSE( AC_LANG_POP]) if test yes != "$lt_cv_cc_needs_belf"; then # this is
probably gcc 2.8.0, egcs 1.0 or newer; no need for -belf CFLAGS=$SAVE_CFLAGS fi ;;*-*so
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                                     strmhz(temp, gd->cpu_clk));      lc
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local void fixedtables OF((struct inflate_state FAR *state));
local int updatewindow OF

```

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 PM9261 board.  
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 <asm/io.h>#include <asm/arch/fsl\_serdes.h>#include <asm/arch/soc.h>  
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 TI's signGP.c \*  
 Copyright (c) 2013, Google Inc. \* Written by Simon Glass <sjg@chromium.org> \* \* Perform a grep of an  
 FDT either displaying the source subset or producing \* a new .dtb subset which can be used as r  
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 <azu@sysgo.de> \*/  
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 mcf5272sim.h of uCLinux distribution:  
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 <common.h>#include <malloc.h>#include <asm/io.h>#include <linux/errno.h>  
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 <remoteproc.h>  
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 \*/#include <common.h>#include <errno.h>#include <dm.h>#include <dm/uclass-internal.h>#include  
 <power/reg  
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 <common.h>#include <command.h>#include <part.h>  
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<asm/gpio
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ARMV7_H#define ARMV7_H
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Copyright (C) 1995-1999, 2000 Free Software Foundation, Inc. * This file is part of the GNU C Library. */
Copyright (C) 2014 Google, Inc */#include <common.h>#include <dm.h>#include <errno.h>#include
<malloc.h>#include <pci.h>#include <dm/device-internal.h>#include <dm/lists.h>#include "pci_inter
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 Copyright (C) 2013 - 2017 Markus Niebel <Markus.Niebel@tq-group.com> \* \* Configuration settings for the  
 TQ Systems TQMa6<Q,D,DL,S> module on \* MBa6 starter kit  
 Copyright (C) 2013, Texas Instruments, Incorporated - http://www.ti.com/ \*/#include <config.h>#include  
 <phy.h>  
 (C) Copyright 2010 \* Texas Instruments, <www.ti.com> \* \* Aneesh V <aneesh@ti.com> \*  
 Sricharan R <r.sricharan@ti.com>  
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 \_XTENSA\_POSIX\_TYPES\_H  
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 Copyright (C) 2011 \* Corscience GmbH & Co. KG - Simon Schwarz <schwarz@corscience.de> \*/#ifndef

```

_NAND_SPL_H_#define _NAND_SPL_H_
Copyright (C) 2013 - ARM Ltd * Author: Marc Zyngier <marc.zyngier@arm.com> * * Based on code by Carl
van Schaik <carl@ok-labs.com>.
(C) Copyright 2002 * Wolfgang Denk, DENX Software Engineering, wd@denx.de. */#ifndef
_CPU_ASM_H#define _CPU_ASM_H
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Based on AT91SAM9261 datasheet revision D.
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(C) Copyright 2015 Vishnu Patekar <vishnupatekar0510@gmail.com> * from sun6i.h */
Copyright (C) 2003-2005 Nokia Corporation * Written by Juha Yrjölä <juha.yrjola@nokia.com> */#ifndef
_GPIO_H#define _GPIO_H
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(C) Copyright 2015 Vishnu Patekar <vishnupatekar0510@gmail.com> * * Based on axp221.c
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Mohammed Khasim <x0khasim@ti.com> *
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Copyright (C) 2009 Samsung Electronics * Minkyu Kang <mk7.kang@samsung.com> */#include
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Copyright (C) 2009 Marc Kleine-Budde <mkl@pengutronix.de> *
(C) Copyright 2010 * Texas Instruments, <www.ti.com> * Aneesh V <aneesh@ti.com> */#ifndef
_UTILS_H_#define _UTILS_H_
(C) Copyright 2000-2013 * Wolfgang Denk, DENX Software Engineering, wd@denx.de. *
(C) Copyright 2014-2018 * Marcel Ziswiler <marcel@ziswiler.com> */
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Configuration settings for the TI EVM5430 board. * See ti_omap5_common.h for omap5 common settings.
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Version R1p10f2) * * This file is based on: drivers/i2c/zynq_i2c.c, * with added driver-model s
(C) Copyright 2004 * Gary Jennejohn, DENX Software Engineering, <garyj@denx.de> */
Copyright (C) 2013 Marek Vasut <marex@denx.de> */#ifndef __CONFIGS_MX23_OLINUXINO_H__#define
__CONFIGS_MX23_OLINUXINO_H__
Copyright (C) 2014 Marek Vasut <marex@denx.de> * * Command for en/de-crypting block of memory with
AES-128-CBC cipher.
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<gores@marvell.com> */
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prototype, previously defined in common.h
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to this * project.
Copyright (C) 1999, 2002-2003 Hewlett-Packard Co. * David Mosberger-Tang <davidm@hpl.hp.com>
* Stephane Eranian <eranian@hpl.hp.com> * * From include/linux/efi.h in kernel 4.1 with some
additions/
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Copyright 2015 Freescale Semiconductor, Inc. \*/#include <common.h>#include <asm/io.h>#include <netdev.h>#include <fm\_eth.h>#include <fsl\_dtsec.h>#include <fsl\_mdio.h>#include <malloc.h>

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Shankar <uma.shankar@samsung.com> \* Manjunatha C Achar <a.manjunatha@samsung.com> \* \* Data  
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\*/

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 <environment.h>#include <mmc.h>#include <u-boot/md5.h>#include <usb.h>#include <watchdog.h>  
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 src/soc/intel/braswell/include/soc/gpio.h  
 Copyright (C) 2013 Stefan Roese <sr@denx.de> \* \* Configuration settings for the ProjectionDesign / Barco  
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 Copyright (c) 2016 Rockchip, Inc. \* Authors: Daniel Meng <daniel.meng@rock-chips.com> \*/#include  
 <common.h>#include <dm.h>#include <malloc.h>#include <usb.h>#include <watchdog.h>#include  
 <linu  
 Copyright 2017 NXP \*/#include <fsl-mc/fsl\_mc\_sys.h>#include <fsl-mc/fsl\_mc\_cmd.h>#include <fsl-  
 mc/fsl\_dpbp.h>  
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 \_led\_display\_h\_#define \_led\_display\_h\_  
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 Copyright (C) 2012-2013 Texas Instruments, Inc. \* \* Based on dfu\_mmc.c which is:  
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 Martin Lundholm <martin.xa.lundholm@stericsson.com> \* Ported to drivers/mmc/ by: Matt Waddel <matt.  
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 \_\_DA8XX\_MUSB\_H\_\_  
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 <errno.h>#include <netdev.h>#include <fsl\_ifc.h>#include <fsl\_ddr.h>#include <fsl\_sec.h>#include  
 <asm/io.h>  
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 <asm/io.h>  
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 hardware it's running on. See Documentation/arm/Setup \* for more info.  
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 <asm/omap\_  
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 Copyright (c) 2013 Corscience GmbH \* Andreas Bießmann <andreas.biessmann@corscience.de> \*/#include  
 <common.h>#include <status\_led.h>#include <twl4030.h>#include <asm/arch/cpu.h>#include <asm/io  
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 (C) Copyright 2010 \* Texas Instruments Incorporated, <www.ti.com> \* Aneesh V <aneesh@ti.com> \*  
 Steve Sakoman <steve@sakoman.com> \*/#include <common.h>#include <twl6030.h>#include <asm/ar  
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 <weth@inf.u-szeged.hu>, \* Patrik Kluba <pajko@halom.u-szeged.hu>, \*  
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 Copyright (C) 2003-2005 Nokia Corporation \* Written by Juha Yrjölä <juha.yrjola@nokia.com> \*/#include  
 <common.h>#include <dm.h>#include <fdtdec.h>#include <asm/gpio.h>#include <asm/io.h>#inclu  
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 <common.h>#include <power/pmic.h>#include <power/max8997\_muic.h>#include  
 <power/hi6553\_pmic.h>  
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 <errno.h>#include <wait\_bit.h>#include <asm/io.h>  
 Copyright (C) 2008 - 2012 Renesas Solutions Corp.  
 (C) Copyright 2007 \* Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com> \*  
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 Copyright (C) 2017 Marek Vasut \*/  
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mc/fsl\_mc\_cmd.h>#include <fsl-mc/fsl\_dpmng.h>#include "fsl\_dpmng\_cmd.h"

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Copyright 2015 Freescale Semiconductor \*/#include <common.h>#include <malloc.h>#include <errno.h>#include <netdev.h>#include <fsl\_ifc.h>#include <fsl\_ddr.h>#include <asm/io.h>#include <fdt\_su

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Modules. \* No standalone port yet available \* - this file is included by both integratorap.h & in

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<dm.h>#include <errno.h>#include <linux/libfdt.h>#include <os.h>#include <asm/io.h>#include  
<asm/setjmp.

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EFI\_SIMPLE\_TEXT\_OUTPUT\_PROTOCOL.

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<common.h>#include <errno.h>#include <fastboot.h>#include <malloc.h>#include  
<linux/usb/ch9.h>#includ

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 (C) Copyright 2011 - 2012 Samsung Electronics \* EXT4 filesystem implementation in Uboot by \* Uma Shankar <uma.shankar@samsung.com> \* Manjunatha C Achar <a.manjunatha@samsung.com>  
 (C) Copyright 2013 \* Andre Przywara, Linaro <andre.przywara@linaro.org> \* \* Routines to transition ARMv7 processors from secure into non-secure state \* and from non-secure SVC into HYP mode \* nee  
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 (36. Ethernet MAC (EMAC))  
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 Copyright (c) 2015, Linaro Limited \*/#include <linux/linkage.h>#include <linux/arm-smccc.h>#include  
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 Copyright 2012 Freescale Semiconductor, Inc. \* Roy Zang <tie-fei.zang@freescale.com> \*/#include <common.h>#include <phy.h>#include <fm\_eth.h>#include <asm/io.h>#include <asm/immap\_85xx.h>#incl  
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 (C) Copyright 2010 \* Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de \* \* Definitions for the SoC:  
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 Copyright (C) 2014 Google, Inc \*/#include <common.h>#include <dm.h>#include <errno.h>#include <fdtdec.h>#include <malloc.h>#include <pch.h>#include <asm/cpu.h>#include <asm/intel\_regs.h>#inc



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 \*/#include <common.h>#include <errno.h>#include <dm.h>#include <dm/uclass-internal.h>#include <power/pmi  
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 \_PANDORA\_H\_  
 (C) Copyright 2010 \* ISEE 2007 SL, <www.iseebcn.com> \*/#include <common.h>#include  
 <status\_led.h>#include <dm.h>#include <ns16550.h>#include <twl4030.h>#include  
 <netdev.h>#include <spl.h>#in  
 Copyright (C) 2015 Cogent Embedded, Inc. \*/  
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 space: \* the physical address at which certain devices (PCIe, NOR, NAND,  
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 (C) Copyright 2007-2008 \* Stelian Pop <stelian@popies.net> \* Lead Tech Design  
 <www.leadtechdesign.com> \* \* Configuration settings for the AT91SAM9M10G45EK board(and  
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 copyright \* \* linux/arch/arm/plat-omap/gpio.c \* \* Support functions for OMAP GPIO  
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 <asm/arch/immap\_ls102xa.h>#include <ahci.h>#include <scsi.h>  
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 \_\_ASM\_ARCH\_MX6\_PINS\_H\_\_  
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 definitions for SPI on TI boards.  
 (C) Copyright 2017 Adaptrum, Inc. \* Written by Alexandru Gagniuc <alex.g@adaptrum.com> for Adaptrum,  
 Inc. \*/  
 Copyright (C) 2011 Michal Simek <monstr@monstr.eu>  
 Copyright (c) 2017 Heinrich Schuchardt <xypron.glpk@gmx.de> \* \* Test the block image transfer in the  
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 <command.h>#include <console.h>#include <g\_dnl.h>#include <fastboot.h>#include <net.h>#include  
 <u  
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 and 3430 SDP code by \* Richard Woodruff <r-woodruff2@ti.com> \* Syed Mohammed Khasim  
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 Montefoschi <francesco.monte@gmail.com>  
 Copyright (C) 2007, Guennadi Liakhovetski <lg@denx.de> \* \* Configuration for the MX35pdk Freescale  
 board.  
 Copyright (C) 2011 \* Heiko Schocher, DENX Software Engineering, hs@denx.de. \*/#ifndef  
 \_\_DM365\_LOWLEVEL\_H\_#define \_\_DM365\_LOWLEVEL\_H\_  
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 Linux by \* Hema HK <hemahk@ti.com>  
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 \*/#ifndef \_\_ASM\_NIOS2\_GLOBALDATA\_H\_#define \_\_ASM\_NIOS2\_GLOBALDATA\_H\_  
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 Copyright (c) 2003, Motorola Inc. \* Xianghua Xiao (X.Xiao@motorola.com) \* \* MPC8260 Communication  
 Processor Module.  
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 <dm/device.h>#include <linux/io.h>#include <linux/sizes.h>#include <malloc.h>#include  
 <mmc.h>#include <sdhci.  
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 <www.leadtechdesign.com> \* \* Configuration settings for the AT91SAM9260EK & AT91SAM9G20EK boards.  
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 Copyright (C) 1999, 2000 Silicon Graphics, Inc. \*/#ifndef \_ASM\_PROCESSOR\_H#define  
 \_ASM\_PROCESSOR\_H  
 Copyright (C) 2003 Robert Schwebel <r.schwebel@pengutronix.de>, Pengutronix  
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 src/arch/x86/boot/pirq\_routing.c  
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 <debug\_uart.h>#include <spl.h>  
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 Express. Parts were derived from other ARM \* configurations.  
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 <riverful.kim@samsung.com> \*/  
 Copyright (c) 2000 Wolfgang Denk <wd@denx.de>  
 Copyright (c) 2016 Google, Inc \* \* Simple program to create a \_dt\_ucose\_base\_size symbol which can be  
 read \* by binutils. This is used by binman tests.  
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 (C) Copyright 2015 Rockchip Electronics Co., Ltd \*/#ifndef \_\_CONFIG\_RK3036\_COMMON\_H#define  
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 \_\_CONFIG\_SOCFPGA\_SR1500\_H\_\_  
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 Copyright (C) 2009 Red Hat \*/#include <common.h>#include <asm/gpio.h>#include <asm/io.h>#include  
 <asm/arch/imx-regs.h>#include <asm/arch/iomux-mx25.h>  
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 \*/  
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```

<asm/arch/soc.h>#ifde
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<common.h>#include <errno.h>#include <dm.h>#include <adc.h>#include <asm/arch/adc.h>
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"pblimage.h"#include "pbl_crc32.h"
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Extent
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 Kernel driver  
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 <common.h>#include <errno.h>#include <phy.h>  
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 <leafy.myeh@allwinnertech.com> \* \* MMC register definition for allwinner sunxi platform.  
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 Copyright 2016 Freescale Semiconductor, Inc. \*/#include <common.h>#include <phy.h>#include  
 <fm\_eth.h>#include <asm/io.h>#include <asm/arch/fsl\_serdes.h>  
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 Copyright 2013 Freescale Semiconductor \* Author: Mingkai Hu <Mingkai.hu@freescale.com> \* Po Liu  
 <Po.Liu@freescale.com> \* \* This file provides support for the board-specific CPLD used on so  
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 Ported to 2.6 kernel \*  
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 (C) Copyright 2003, Motorola, Inc. \* maintained by Xianghua Xiao (x.xiao@motorola.com) \* author Andy  
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 Zhang Wei<wei.zhang@freescale.com> \* \* with the reference on libata and ahci drvier in kern  
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 <asm/arch/imx-regs.h>#include <asm/mach-imx/rdc-sema.h>#include <asm/arch/imx-rdc.h>#include <l  
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 <linux/types.h>#include <asm/io.h>#include <asm/armv7.h>#include <asm/pl310.h>#include  
 <config.h>#in  
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 (C) Copyright 2009 \* Marvell Semiconductor <www.marvell.com> \* Written-by: Prafulla Wadaskar  
 <prafulla@marvell.com> \* \* Header file for Feroceon CPU core 88F5182 SOC.  
 (C) Copyright 2010 \* Texas Instruments, <www.ti.com> \* Aneesh V <aneesh@ti.com> \*/#ifndef  
 \_PL310\_H\_#define \_PL310\_H\_  
 Copyright: Copyright (C) 2001, Russ Dill \* Author: Russ Dill <Russ.Dill@asu.edu> \* Description:  
 Module to load kernel from jffs2 \*-----  
 (C) Copyright 2007-2011 \* Allwinner Technology Co., Ltd. <www.allwinnertech.com> \* Tom Cubie  
 <tangliang@allwinnertech.com> \* \* Some init for sunxi platform.  
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 Copyright (C) 2012 Altera Corporation <www.altera.com> \*/#ifndef  
 \_\_CONFIG\_SOCFPGA\_COMMON\_H\_\_#define \_\_CONFIG\_SOCFPGA\_COMMON\_H\_\_  
 Copyright (c) 2015 Google, Inc \* Written by Simon Glass <sjg@chromium.org> \* \* SMBus block read/write  
 support added by Stefan Roese:  
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 <esw@bus-elektronik.de> \*/  
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 mx25/mx25\_pins.h:  
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 Perkovic <luka.perkov@sartura.hr> \*/  
 Copyright (C) 2011 \* Heiko Schocher, DENX Software Engineering, hs@denx.de. \*/#include  
 <common.h>#include <nand.h>#include <ns16550.h>#include <post.h>#include <asm/ti-  
 common/davinci\_nand.h>#i  
 Copyright (C) 2016 Rockchip Electronic Co.,Ltd \* Written by Kever Yang <kever.yang@rock-chips.com>  
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 \* NOTE: \*  
 Copyright (c) Thomas Gleixner <tglx@linutronix.de> \*/#ifndef \_\_UBOOT\_UBISPL\_H#define  
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 (C) Copyright Linus Torvalds 1999  
 Copyright (c) 2002 Alex Z  
 Copyright (c) 2010 Albert AribaUD. \*/  
 Copyright 2012 Stefan Roese <sr@denx.de> \*/#include <common.h>#include <config.h>#include  
 <spl.h>#include <image.h>#include <linux/compiler.h>  
 (C) Copyright 2000-2004 \* DENX Software Engineering \* Wolfgang Denk, wd@denx.de \* \* Updated-by:  
 Prafulla Wadaskar <prafulla@marvell.com> \* default\_image specific code abstracted from



mkimage.c  
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 (C) Copyright 2011-2012 \* Pali Rohár <pali.rohar@gmail.com> \*/  
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 (C) Copyright 2012 \* Ilya Yanok <ilya.yanok@gmail.com> \*/#include <common.h>#include <errno.h>#include <spl.h>#include <net.h>#include <linux/libfdt.h>  
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 (C) Copyright 2004 \* Wolfgang Denk, DENX Software Engineering, wd@denx.de. \* \* This code is based on linux driver for sl811hs chip, source at \* drivers/usb/host/sl811.c: \*  
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 \*/  
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 \_ASM\_SPL\_H\_  
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 http://compulab.co.il/ \* Bernecker & Rainer Industrieelektronik GmbH - http://www.br-automation.com \*/  
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 Meson Successive Approximation Register (SAR) A/D Converter  
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 Copyright (C) 2015, Bin Meng <bmeng.cn@gmail.com> \* \* Ported from Intel released Quark UEFI BIOS \*  
 QuarkSocPkg/QuarkNorthCluster/MemoryInit/Pei  
 (C) Copyright 2014 DENX Software Engineering GmbH \* Written-by: Albert ARIBAUD  
 <albert.aribaud@3adev.fr> \* \* Low level init is called from SPL to set up the clocks. \* On entry, the  
 LPC3250 is in  
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 <philipp.tomsich@theobroma-systems.com> \*/  
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 \_\_CONFIG\_SOCFPGA\_SOCRATES\_H\_\_#define \_\_CONFIG\_SOCFPGA\_SOCRATES\_H\_\_  
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 Copyright (C) 2006 \* Dominic Rath <Dominic.Rath@gmx.de> \*  
 Copyright 2014 Freescale Semiconductor \*/#include <common.h>#include <malloc.h>#include  
 <errno.h>#include <netdev.h>#include <fsl\_ifc.h>#include <fsl\_ddr.h>#include <asm/io.h>#include  
 <fdt\_su  
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 Copyright (C) 2015 Marek Vasut <marex@denx.de> \*/#ifndef \_\_CONFIG\_TERASIC\_DE0\_H\_\_#define  
 \_\_CONFIG\_TERASIC\_DE0\_H\_\_  
 Copyright (C) 2018 Marek Vasut <marex@denx.de> \*/#ifndef  
 \_\_CONFIG\_DEVBOARDS\_DBM\_SOC1\_H\_\_#define \_\_CONFIG\_DEVBOARDS\_DBM\_SOC1\_H\_\_  
 (C) Copyright 2000 \* Sysgo Real-Time Solutions GmbH \* Klein-Winternheim, Germany \*-----  
 -----  
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 Copyright (c) 2011, Google Inc. All rights reserved  
 Copyright 2017 NXP \*/#include <common.h>#include <dm.h>#include  
 <dm/platform\_data/pfe\_dm\_eth.h>#include <net.h>#include <net/pfe\_eth/pfe\_eth.h>  
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 Abraham I <kishon@ti.com>  
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 <tangliang@allwinnertech.com> \* \* Configuration settings for the Allwinner A10-evb board.  
 Copyright (c) 2017 Heinrich Schuchardt <xypron.glpk@gmx.de> \* \* This unit test uses timer events to  
 check the implementation \* of the following boottime services:  
 Copyright (C) 1996-2005 Markus F.X.J. Oberhumer <markus@oberhumer.com> \* \* The full LZO package  
 can be found at:  
 Copyright (c) 2002 Alex Z \*/  
 (C) Copyright 2009 DENX Software Engineering \* Author: John Rigby <jrigby@gmail.com> \* \* Common  
 asm macros for imx25  
 (C) Copyright 2012-2014 \* Texas Instruments Incorporated, <www.ti.com> \*/#include  
 <common.h>#include <asm/io.h>#include <asm/ti-common/keystone\_nav.h>  
 (C) Copyright 2013 Luke Kenneth Casson Leighton <lkcl@lkcl.net> \*/  
 Copyright (C) 2016 Google, Inc \* Written by Amar <amarendra.xt@samsung.com> \*/  
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 (C) Copyright 2013 Maxime Ripard <maxime.ripard@free-electrons.com> \* \* Configuration settings for the  
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Copyright 2004, 2007, 2008 Freescale Semiconductor. \* Srikanth Srinivasan <srikanth.srinivaan@freescale.com> \*/#include <config.h>#include <mpc86xx.h>

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 Copyright (C) 2013 Ken Kuo (ken\_kuo@andestech.com) \* \* Structure passed to kernel to tell it about the \*  
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 (C) Copyright 2013 Patrice Bouchand <pbfwdlist\_gmail\_com> \* lzma uncompress command in Uboot \* \*  
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 \*\*\*\*\* \* NOTE: This header  
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 <asm/arch/mem.h>#include <asm/arch/sys\_proto.h>#include "overo.h"  
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 Copyright: Copyright (C) 2001, Russ Dill \* Author: Russ Dill <Russ.Dill@asu.edu> \* Description:  
 header for load kernel modules \*-----  
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 Copyright (C) 2009 Texas Instruments Incorporated - http://www.ti.com/ \* \* Driver for SPI controller on  
 DaVinci. Based on atmel\_spi.c \* by Atmel Corporation  
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 SH7750/SH7750S/SH7750R/SH7751/SH7751R \* Internal I/O register  
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 <dm.h>#include <ns16550.h>#include <netdev.h>#include <twl4030.h>#include  
 <linux/mtd/rawnand.h>#include

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 keith\_outwater@mvis.com` \*/

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 <common.h>#include <nand.h>#include <malloc.h>#include "mxs\_nand.h"

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 Thumb mode. However, in U-Boot \* we don't use interrupt context, so we can redefine these a

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 <asm/io.h>#include <fdt\_support.h>#include <fsl\_mdio.h>#ifdef CONFIG\_FSL\_LAYERSCAPE#include <a  
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 Copyright (C) 1995-2005 Jean-loup Gailly and Mark Adler  
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 Copyright (c) 2008, Excito Elektronik i Sk  
 Copyright (c) 2011 The Chromium OS Authors. \* \* Modified from Linux arch/arm/include/asm/bitops.h  
 (C) Copyright 2009 \* Stefano Babic, DENX Software Engineering, sbabic@denx.de. \*/#ifndef \_\_SYS\_PROTO\_IMX5\_\_#define \_\_SYS\_PROTO\_IMX5\_\_  
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 Copyright (c) 2014 Google, Inc \* \* From Coreboot file cpu/intel/model\_206ax/cache\_as\_ram.inc  
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Copyright (c) 2013 Lubomir Popov <lpopov@mm-sol.com>, MM Solutions \* New i2c\_read, i2c\_write and i2c\_probe functions, tested on OMAP4 \* (4430/60/70), OMAP5 (5430) and AM335X (3359); should work on o

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(C) Copyright 2010 \* Texas Instruments, <www.ti.com> \* Aneesh V <aneesh@ti.com> \*/#include <linux/types.h>#include <common.h>#include <asm/armv7.h>#include <asm/utlis.h>

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 \* the LAN91C96 single chip ethernet controller. It is a modified \* version of the smc911  
 (C) Copyright 2015 Mateusz Kulikowski <mateusz.kulikowski@gmail.com> \*/#include  
 <common.h>#include <dm.h>#include <power/pmic.h>#include <spmi/spmi.h>  
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 (C) Copyright 2001 \* Hans-Joerg Frieden, Hyperion Entertainment \* Hans-JoergF@hyperion-  
 entertainment.com \*/#include <common.h>#include <command.h>#include <ide.h>#include  
 "part\_amiga.h"  
 Copyright (C) 2008 by NXP Semiconductors \* @Author: Kevin Wells \* @Descr: Definitions for LPC3250 chip  
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 electronics.com> \* \* based on code of fs/reiserfs/dev.c by  
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 Copyright (c) 2003 Motorola, Inc. \* \* In addition to the individual control of the communication \* channels,  
 there are a few functions that globally affect the \* communication processor.  
 Copyright (C) 2017 Andes Technology \* Chih-Mao Chen <cmchen@andestech.com> \* \* Statically process  
 runtime relocations on RISC-V ELF images \* so that it can be directly executed when loaded at LMA  
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 \_SUNXI\_I2C\_H\_

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 Copyright (C) 2012 ARM Ltd. \* \* arch/arm64/kernel/head.S:  
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 (C) Copyright 2016 \* Rick Chen, NDS32 Software Engineering, rick@andestech.com \*/#include <common.h>#include <dm.h>#include <errno.h>#include <timer.h>#include <linux/io.h>  
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 Copyright (C) 2014 Samsung Electronics \* \* Configuration settings for the SAMSUNG/GOOGLE PEACH-PI board.  
 Copyright 2015 Freescale Semiconductor \*/#include <common.h>#include <malloc.h>#include <errno.h>#include <netdev.h>#include <fsl\_ifc.h>#include <fsl\_ddr.h>#include <asm/io.h>#include <hwconf  
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\_BOOTCOUNT\_H\_\_#define \_BOOTCOUNT\_H\_\_

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<vbe.h>#include <linux/linkage.h>#include <asm/cache.h>#include <asm/processor.h>#include  
<asm/i825

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<dm.h>#include <dm/platform\_data/serial\_pl01x.h>#include <errno.h>#include <malloc.h>#include

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 (C) Copyright 2001 \* ARIOD Data Networks, Inc. dchiu@ariodata.com \* \* Based on MontaVista DS1743 code  
 and U-Boot mc146818 code  
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 <errno.h>#include <asm/io.h>#include <asm/arch/clock.h>#include <asm/arch/dram.h>#include  
 <asm/arch/prcm.h>  
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 linux kernel  
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 <common.h>#include <dm.h>#include <errno.h>#include <remoteproc.h>#include <dm/test.h>#include  
 <test/ut.h>  
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 Copyright (C) 1999 ARM Limited. \*/  
 Copyright (C) 2013 Boundary Devices Inc. \*/#ifndef \_\_ASM\_ARCH\_MX6Q\_DDR\_H\_\_#define  
 \_\_ASM\_ARCH\_MX6Q\_DDR\_H\_\_  
 Copyright 2011 Freescale Semiconductor, Inc. \*/#include <common.h>#include <mpc85xx.h>#include

```

<asm/io.h>#include <ns16550.h>#include <nand.h>#include <asm/mmu.h>#include
<asm/immap_85xx.h>#i
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Mohammed Khasim <x0khasim@ti.com> * Nishanth Menon <nm@ti.com> * * Configuration settings for
the TI O
(C) Copyright 2012-2014 * Texas Instruments Incorporated, <www.ti.com> */#ifndef
_PSC_DEFS_H_#define _PSC_DEFS_H_
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<jjhiblot@ti.com> */
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Copyright (c) 2001 MIPS Technologies, Inc. */#ifndef _ASM_STRING_H_#define _ASM_STRING_H
Copyright (C) 1998 Gabriel Paubert. */
(C) Copyright 2015 Hans de Goede <hdegoede@redhat.com> * * Configuration settings for the Allwinner
A80 (sun9i) CPU
Copyright (C) 2008 by NXP Semiconductors * @Author: Based on code by Kevin Wells * @Descr: USB
driver - Embedded Artists LPC3250 OEM Board support functions *
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<pci.h>#include <asm/io.h>#include "integrator-sc.h"#include "pci_v3.h"
Copyright (C) 1994-2013 Russell King */#include <linux/serial_reg.h>
(C) Copyright 2010,2011 * Graeme Russ, <graeme.russ@gmail.com> * * Portions from Coreboot
mainboard/google/link/romstage.c
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(C) Copyright 2002 * Rich Ireland, Enterasys Networks, rireland@enterasys.com. * * ispVM functions
adapted from Lattice's ispmVMEEmbedded code:
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arch/powerpc/include/asm/mpc85xx_gpio.h, which is
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is free software; you can redistribute it and/or modify * it under the terms of the GNU Gen
Copyright (C) 2018 Marek Vasut <marek.vasut@gmail.com> * * Clock configuration based on Linux i2c-
rcar.c:
Copyright (C) 1996-1999 SciTech Software, Inc.
Copyright (C) 2017 Google, Inc * Written by Simon Glass <sjg@chromium.org> * * This file is required for
SPL to build, but is empty.
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Copyright (C) 2008 Nobuhiro Iwamatsu * * Based on board/renesas/rsk7203/lowlevel_init.S

```



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 Copyright (c) 2018 Heinrich Schuchardt <xypron.glpk@gmx.de> \* \* This unit test checks the following protocol services:  
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 Copyright (C) 2016 Paul Kocalkowski <contact@paulk.fr> \*/  
 Copyright (C) 2003 Bernardo Innocenti <bernie@develer.com> \* Based on former asm-ppc/div64.h and asm-m68knommu/div64.h \* \* Optimization for constant divisors on 32-bit machines:  
 Copyright 2000-2002 by Hans Reiser, licensing governed by reiserfs/README \* \* GRUB -- GRand Unified Bootloader  
 Copyright (C) 2013,2014 - ARM Ltd \* Author: Marc Zyngier <marc.zyngier@arm.com> \* \* This program is free software; you can redistribute it and/or modify \* it under the terms of the GNU General Pub  
 (C) Copyright 2014 Topic Embedded Products \* \* Configuration for Zynq Evaluation and Development Board - Miami \* See zynq-common.h for Zynq common configs  
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 (tgx@linutronix.de) \* \* Credits:  
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 sources and earlier U-Boot Allwinner A10 SPL work  
 Copyright (C) 2004 by David Brownell \* updates to merge with Linux 2.6, better match RNDIS  
 spec \*/  
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 <ash@gumstix.com> \* \* Configuration settings for the Gumstix DuoVero board. \* See omap4\_common.h  
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 <asm/io.h>#include <a  
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 (C) Copyright 2016 \* Author: Amit Singh Tomar, amittomer25@gmail.com \* \* Ethernet driver for  
 H3/A64/A83T based SoC's  
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 (C) Copyright 2010 \* Matt Waddel, <matt.waddel@linaro.org> \*/#ifndef \_WDT\_H\_#define \_WDT\_H\_  
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 <div64.h>#include <watchdog.h>#include <asm/arch/fpga\_manager.h>#include  
 <asm/arch/sdram.h>#include <  
 (C) Copyright 2008 Atmel Corporation \*/#include <common.h>#include <dm.h>#include  
 <environment.h>#include <malloc.h>#include <spi.h>#include <spi\_flash.h>#include  
 <search.h>#include <errno.h>  
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 file to generate boot.scr - a boot script for U-Boot# Generate boot.scr:  
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 (C) Copyright 2016 Broadcom Ltd. \*/#include <common.h>#include <asm/system.h>#include  
 <asm/armv8/mmu.h>  
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 <bhupesh.sharma@freescale.com> \*/#include <common.h>#include <dm.h>#include  
 <malloc.h>#include <errno.h>#include <netd  
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 <dm.h>#include <phy.h>  
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 <common.h>#include <config.h>#include <spl.h>#include <asm/io.h>#include <nand.h>#include <linu  
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 <andreas.biessmann@corscience.de> \*/#include <common.h>#include <i2c.h>  
 Copyright (C) 2015 Freescale Semiconductor, Inc \* Peng Fan <Peng.Fan@freescale.com> \*/#ifndef  
 \_\_PFUZE3000\_PMIC\_H\_#define \_\_PFUZE3000\_PMIC\_H\_  
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 <errno.h>#include <malloc.h>  
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 definitions for MMC/SD on TI boards.  
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 <common.h>#include <errno.h>#include <dm.h>#include <adc.h>#include <sandbox-adc.h>

Copyright 2017 NXP \*/#include <common.h>#include <errno.h>#include <linux/bug.h>#include  
<asm/io.h>#include <linux/libfdt.h>#include <net.h>#include <fdt\_support.h>#include <fsl-  
mc/fsl\_mc.h>

Copyright 2009 Benjamin Herrenschmidt, IBM Corp \* benh@kernel.crashing.org \* \* Based on parts of  
drivers/of/fdt.c from Linux v4.9 \* Modifications for U-Boot

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Copyright 2014\n"); if (info->model[0]) { printf("Model: %s\n", info->model);  
printf("MFGDate: %02x-%02x-%02x%02x\n", info->mfgdate[0], info-  
>mfgdate[1], info->mfgdate[2], info-

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<asm/arch/clock.h>#include <asm/arch/imx-regs.h>#include <asm/arch/sys\_proto.h>#include  
<asm/mach-imx/hab.h>

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src/soc/intel/baytrail/include/soc/pci\_devs.h

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```

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regs.h>#include
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provides support for the ngPIXIS, a board-specific FPGA used on * some Freescale reference boar
(C) Copyright David Brownell 2000 (kernel hotplug, usb_device_id)
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Copyright (C) 2007 Atmel Corporation */#include <common.h>#include <clk.h>#include <dm.h>#include
<fdtdec.h>#include <spi.h>#include <malloc.h>#include <wait_bit.h>
Copyright 2016 NXP Semiconductor. * Author: Wang Dongsheng <dongsheng.wang@freescale.com> */
(C) Copyright 2008-2014 Rockchip Electronics * Peter, Software Engineering, <superpeter.cai@gmail.com>.
*/#ifndef _ASM_ARCH_CRU_RK3288_H#define _ASM_ARCH_CRU_RK3288_H
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*/
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electrons.com> */
Copyright (c) 2018 Heinrich Schuchardt <xypron.glpk@gmx.de> * * This test checks the StartImage boot
service. * The efi_selftest_miniapp_exit.efi application is loaded into memory * and started.
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drivers/crypto/caam
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```



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 <asm/arch/imx-regs.h>#include <asm/arch/iomux.h>  
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 <common.h>#include <nand.h>#include <ns16550.h>#include <post.h>#include  
 <asm/arch/da850\_lowlevel.h>#incl  
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 Copyright (C) 2012 Boundary Devices Inc. \*/#include <common.h>#include <linux/errno.h>#include  
 <asm/io.h>#include <asm/mach-imx/boot\_mode.h>#include <malloc.h>#include <command.h>  
 Copyright (C) 2014 Marek Vasut <marex@denx.de> \*/#ifndef \_\_CONFIG\_SOCFPGA\_ARRIA5\_H\_\_#define  
 \_\_CONFIG\_SOCFPGA\_ARRIA5\_H\_\_

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 \*/#undef DEBUG  
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 by the mips-relocs \* tool & contains a record of all locations in the U-Boot binary tha  
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 <netdev.h>#include <fm\_eth.h>#include <fsl\_dtsec.h>#include <fsl\_mdio.h>#include <malloc.h>  
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 specific com  
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 <asm/io.h>#i

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 <asm/arch/at91\_pmc.h>#include <asm/arch

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 <asm/io.h>#include <asm/arch/imx-regs.h>#include <asm/mach-imx/iomux-v3.h>#include <asm/mach-  
 imx/sys\_proto.h>

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<asm/arch/clock.h>#include <asm/ti-common/keystone\_net.h>#include <asm/arch/psc\_defs.h>#inclu  
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 <twl4030.h>#include <asm/io.h>#include <asm/gpio.h>#include <asm/arch/mmc\_host\_def.h>#include  
 <asm/arch/  
 (C) Copyright 2016 \* Jelle van der Waa <jelle@vdwaa.nl> \*/#include <common.h>#include  
 <i2c.h>#include <sy8106a.h>  
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 mx27/imx-regs.h  
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 <fabio.estevam@freescale.com> \* \* Based on m28evk.h:  
 (C) Copyright 2000 \* Wolfgang Denk, DENX Software Engineering, wd@denx.de. \*/#include <common.h>  
 (C) Copyright 2015 Jagan Teki <jteki@openedev.com> \* \* Xilinx Zynq Quad-SPI(QSPI) controller driver  
 (master mode only)  
 Copyright (C) 1992, 1994 by Theodore Ts \* \* These are the UART port assignments, expressed as offsets  
 from the base \* register. These assignments should hold for any serial port based on \* a 825  
 Copyright (C) 2008 Yusuke Goda <goda.yusuke@renesas.com> \* \* u-boot/board/r7780mp/r7780mp.h  
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 Copyright (c) 2017 Heinrich Schuchardt <xypron.glpk@gmx.de> \* \* This unit test checks that the  
 notification function of an \* EVT\_SIGNAL\_EXIT\_BOOT\_SERVICES event is called exactly once.  
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 I2C core functions (wrappers)  
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Copyright (c) 2018 Heinrich Schuchardt <xypron.glpk@gmx.de> \* \* This test checks the StartImage boot service. \* The efi\_selftest\_miniapp\_return.efi application is loaded into memory \* and started.

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 the Serial Download Protocol (SDP) as specified in \* the i.MX 6 Reference Manual. The SDP is a  
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 <asm/arch/clock.h>#include <asm/arch/grf\_rk3368.h>#include <syscon.h>  
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 (srikanth.srinivasan@freescale.com) \*/  
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 <asm/macro.h>  
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 (C) Copyright 2015 Jens Kuske <jenskuske@gmail.com> \*/#include <common.h>#include  
 <asm/io.h>#include <asm/arch/clock.h>#include <asm/arch/dram.h>#include  
 <asm/arch/cpu.h>#include <linux/kc  
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 registers \* and the dual port ram for the Communication Processor Module. \* Within this sp  
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 Copyright (c) 2016 Andreas F \*/  
 Copyright (C) 1999 Ben Williamson <benw@pobox.com> \* \* This program is loaded into SRAM in bootstrap  
 mode, where it waits \* for commands on UART1 to read and write memory, jump to code etc. \* A de  
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 <simon.kagstrom@netinsight.net> \* \* Based on sheevaplug.c:  
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 <common.h>#include <linux/bitops.h>#include <phy.h>  
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 <asm/arch/clock.h>#include <asm/arch/imx-regs.h>#include <linux/errno.h>#include  
 <asm/gpio.h>#include  
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 by  
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 the Exit boot service to return.  
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 <reinhard.arlt@esd-electronics.com> \* Based on board/mpc8349emds/mpc8349emds.c (and previous 834x  
 relea  
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 <mugunthanvnm@ti.com>  
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\_\_CONFIGS\_BG0900\_H\_\_

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returns directly without calling the Exit boot service.

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NDS32\_BOOTM\_H#define NDS32\_BOOTM\_H

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```

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 \*/#include <common.h>#include <dm.h>#include <errno.h>#include <syscon.h>#include  
 <asm/io.h>#inclu  
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 <asm/io.h>#include <dm  
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 \* Adrian Hunter \* Zoltan Sogor  
 Copyright 2015 Freescale Semiconductor, Inc. \*/#include <common.h>#include <phy.h>#include <fsl-  
 mc/ldpaa\_wriop.h>#include <asm/io.h>#include <asm/arch/fsl\_serdes.h>  
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 Mips port b  
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 <asm/processor.h>#include <asm/macro.h>  
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 <asm/arch/  
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 Adapted for OMAP2420 I2C, r-woodruff2@ti.com  
 Copyright (c) 2016 Google, Inc \* \* Simple program to create a bad \_dt\_ucose\_base\_size symbol to create  
 an \* error when it is used. This is used by binman tests.  
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 \_\_RN5T567\_PMIC\_H\_\_#define \_\_RN5T567\_PMIC\_H\_\_

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 src/soc/intel/baytrail/include/soc/iomap.h  
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 <asm/utis.h>#include <nand>.  
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 (C) Copyright 2015 Mateusz Kulikowski <mateusz.kulikowski@gmail.com> \* \* UART will work in Data  
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 <kyungmin.park@samsung.com> \* \* Configuration settings for the SAMSUNG Universal (s5pc100) board.  
 (C) Copyright 2017 Rockchip Electronics Co., Ltd \*/#include <common.h>#include <clk.h>#include  
 <dm.h>#include <dt-structs.h>#include <errno.h>#include <ram.h>#include <regmap.h>#include <sysc>  
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 address lines \* connected as ALE and CLE inputs.  
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 Restructured codes by jsgood (jsgood.yang@samsung.com) \* 2007-09-21 - Added MoviNAND and  
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 need an in memory image of \* a disk.  
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 <fm\_eth.h>#include <asm/io.h>#include <asm/arch/fsl\_serdes.h>  
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\_TIMER\_DEFS\_H\_#define \_TIMER\_DEFS\_H\_  
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Chung.Liew@freescale.com) \*/  
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<lokeshvutla@ti.com>  
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 Copyright (C) 2004, 2005 \* Cory T. Tusar, Videon Central, Inc., <ctusar@videon-central.com> \* \* Based in large part on linux/include/asm-arm/arch-ep93xx/regmap.h, which is  
 Copyright (C) 2013 Samsung Electronics \* Sanghee Kim <sh0130.kim@samsung.com> \* Piotr Wilczek <p.wilczek@samsung.com> \* \* Configuration settings for the SAMSUNG TRATS2 (EXYNOS4412) board.  
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 (C) Copyright 2014 \* Heiko Schocher, DENX Software Engineering, hs@denx.de. \*/#include <common.h>#include <linux/mtd/mtd.h>#include <jffs2/jffs2.h>  
 Copyright (c) 1995-2003 by Internet Software Consortium  
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Copyright 2015 Microchip Inc. \* Purna Chandra Mandal <purna.mandal@microchip.com> \*/#include <common.h>#include <phy.h>#include <miiphy.h>#include <errno.h>#include <wait\_bit.h>#include <asm/i

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 <asm/arch/sys\_proto.h>#include <asm/arch/hardware.h>#include <asm/arch/mux.h>#include  
 <asm/io.h>  
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 copyright)); write\_all(STDOUT\_FILENO, help,  
 strlen(help)); goto newcmd;

}

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Copyright Year and Copyright Data to be user definable (in .tex file). % Gerry Nov. 30th.

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% Default

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copyright year to 2009." cd data ../xorp/devnotes/update\_copyright.sh 2008 cvs diff -u -p | less cvs commit

cd ../other ../xorp/devnotes/update\_copyright.sh 2008 cvs diff -u -p | less cvs comm

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copyright messages <new\_year> The new year. If it is not specified, it will be set to the year after <old\_year>. -h Print usage EOF exit 1}

ydk-models-cisco-ios-xe 16.6.1 pypi:ydk-models-cisco-ios-xe/16.6.1

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zlib 1.2.11 unknown: undefined

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copyright conditions above.; \*; \* Mar-13-2003 -- Most of this is derived from inffast.S which is derived from; \* the gcc -S output of zlib-1.2.0/inffast.c. Zlib-1.2.0 is in beta release at; \*

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Copyright (c) 1997,99 Borland Corp.,and is based on zlib version 1.0.4. There are a series of bugsand security problems associated with that old zlib version, andwe recommend the users to update t

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Copyright (C) 1998 Brian Raiter <breadbox@muppetlabs.com>;;; You can look at <http://www.muppetlabs.com/~breadbox/software/assembly.html>;;;

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Copyright (C) 2003-2010 Mark Adler\n"); fprintf(stderr, "usage: gun [-t] [file1.gz [file2.Z ...]]\n"); return 0; } if (argc && strcmp(\*argv, "-t") == 0) { test = 1;

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Copyright (C) 2003, 2012 Mark Adler\n"); printf( "usage: gzappend [-level] file.gz [ addthis [ andthis ... ]]\n"); return 0;

copyright to help \* - Send help to stdout instead of stderr \* - Add some preemptive typecasts \* - Add L to constants in lseek() calls

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blast.h \* version 1.3, 24 Aug 2013 \* \* blast.c decompresses data compressed by the PKWar  
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 \*/

zsh 5.6.2 debian:zsh/5.6.2-3  
 (C) (wc\_data(C) >> 2)#define WCB\_IF(T,O) wc\_bld(WC\_IF, ((T) | ((O) << 2)))  
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 convenience only and this body has no legal status. The Z shell is distributed under the f  
 (C) wc\_data(C)#define WCB\_TRY(O) wc\_bld(WC\_TRY, (O))  
 (C) wc\_data(C)#define WCB\_REPEAT(O) wc\_bld(WC\_REPEAT, (O))  
 copyright information. None of the core functions are affected by this, so those files may simply be omitted.  
 (C) (wc\_data(C)#define WC\_FOR\_PPARAM 0#define WC\_FOR\_LIST 1#define WC\_FOR\_COND  
 2  
 (C) (wc\_data(C)#define WC\_WHILE\_WHILE 0#define WC\_WHILE\_UNTIL 1  
 (C) wc\_data(C)#define WCB\_SIMPLE(N) wc\_bld(WC\_SIMPLE, (N))  
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 (C) ((wc\_data(C)#define WC\_ASSIGN\_SCALAR 0#define WC\_ASSIGN\_ARRAY 1#define  
 WC\_ASSIGN\_NEW 0/\* \* In normal assignment, this indicate += to append. \* In assignment following a  
 typeset  
 Copyright (c) 1999 Sven Wischnowsky  
 Copyright: 1999 Adam Spiers <adam@spiers.net>  
 Copyright: 2005, 2006 Steve Borho <steve@borho.org>  
 (C) (wc\_data(C) >> WC\_CASE\_FREE)#define WCB\_CASE(T,O) wc\_bld(WC\_CASE, ((T) | ((O) <<  
 WC\_CASE\_FREE)))  
 (C) ((C) >> WC\_CODEBITS)#define wc\_bdata(D) ((D) << WC\_CODEBITS)#define wc\_bld(C,D)  
 (((wordcode) (C)) | (((wordcode) (D)) << WC\_CODEBITS))  
 (C) ((int)(wc\_data(C)#define WCB\_REDIR(T) wc\_bld(WC\_REDIR, (T))/\* Size of redir is 4 words if  
 REDIR\_VARID\_MASK is set, else 3 \*/#define WC\_REDIR\_WORDS(C) \

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 Andrew Main, Peter W. Stephenson, Sven Wischnowsky, and others). All rights reserved  
 (C) (wc\_data(C) >> 2)#define WCB\_FOR(T,O) wc\_bld(WC\_FOR, ((T) | ((O) << 2)))  
 Copyright (c) 2005 Peter Stephenson  
 Copyright: 2009 Nicolas Pouillard  
 Copyright (c) 1998 Peter Stephenson  
 (C) (wc\_data(C)#define WC\_SUBLIST\_END 0#define WC\_SUBLIST\_AND 1#define WC\_SUBLIST\_OR  
 2  
 Copyright (c) 1992-1997 Paul Falstad  
 Copyright (c) 2015 Barton E. Schaefer  
 Copyright (c) 2000 Sven Wischnowsky, Clint Adams  
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 copyright (c) 2017 Sebastian Gniazdowski  
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 Copyright (c) 1999 Peter Stephenson  
 (C) (wc\_data(C)#define WC\_CASE\_HEAD 0#define WC\_CASE\_OR 1#define WC\_CASE\_AND  
 2#define WC\_CASE\_TESTAND 3#define WC\_CASE\_FREE (3) /\* Next bit available in integer  
 (C) (wc\_data(C) >> 1)#define WCB\_SELECT(T,O) wc\_bld(WC\_SELECT, ((T) | ((O) << 1)))  
 Copyright (c) 1998-2003 Peter Stephenson  
 (C) (wc\_data(C) >> 1)#define WCB\_PIPE(T,L) wc\_bld(WC\_PIPE, ((T) | ((L) << 1)))  
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Copyright: 2009 Holger Macht <holger@hmac.de>
(C) (wc_data(C)#define WC_SELECT_PPARAM 0#define WC_SELECT_LIST 1
(C) Steven Singer, * CSR BlueCore firmware, ca. 2000.) * * Hence for now this remains byte-by-
byte.
(C) (wc_data(C)
Copyright (C) 1994 X Consortium
Copyright (c) 2000 Peter Stephenson
Copyright (c) 1996-1997 Sven Wischnowsky
Copyright (c) 2014 Oliver Kiddle
(C) wc_data(C)#define WCB_TYPESET(N) wc_bld(WC_TYPESET, (N))
(C) (wc_data(C) >> WC_SUBLIST_FREE)#define WCB_SUBLIST(T,F,O) wc_bld(WC_SUBLIST, \
((T) | (F) | ((O) << WC_SUBLIST_FREE)))
(C) (wc_data(C)#define WC_SUBLIST_COPROC 4#define WC_SUBLIST_NOT 8#define
WC_SUBLIST_SIMPLE 16#define WC_SUBLIST_FREE (5) /* Next bit available in integer */
(C) (wc_data(C) >> 2)#define WCB_ASSIGN(T,A,N) wc_bld(WC_ASSIGN, ((T) | ((A) << 1) | ((N) <<
2)))
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https://www.zsh.org/pub/ ftp://ftp.zsh.org/pub/ https://sourceforge.net/projects/zsh/fi
Copyright (c) 1997 Andrew Main
(C) wc_data(C)#define Z_END (1<<4) #define Z_SIMPLE (1<<5)#define
WC_LIST_FREE (6) /* Next bit available in integer */
copyright in all changes.Comment:
(C) ((int)(wc_data(C)
(C) flags. int casmod = CASMOD_NONE; /* * quotemod says we are doing either (q/b) (positive), (Q)
(negative) * or not (0). quotetype counts the q's for the first case. * quoter
(C) ((int)(wc_data(C)
(C) (wc_data(C) >> 1)#define WCB_WHILE(T,O) wc_bld(WC_WHILE, ((T) | ((O) << 1)))
(C) wc_data(C)#define WCB_CURSH(O) wc_bld(WC_CURSH, (O))
Copyright (c) 1992-2007 Paul Falstad
(C) (wc_data(C) >> 7)#define WCB_COND(T,O) wc_bld(WC_COND, ((T) | ((O) << 7)))
Copyright (c) 2002 Peter Stephenson
(C) wc_data(C)#define WCB_SUBSH(O) wc_bld(WC_SUBSH, (O))
(C) (wc_data(C)#define WC_IF_HEAD 0#define WC_IF_IF 1#define WC_IF_ELIF
2#define WC_IF_ELSE 3
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(C) (wc_data(C)
Copyright (c) 2007,2012 Phil Pennock
Copyright: 1996 Robert Leslie <rob@mars.org>
copyright of The Zsh Development Group and licensed under
Copyright (c) 1996-1997 Andrew Main
(C) (wc_data(C)#define WC_PIPE_END 0#define WC_PIPE_MID 1
(C) wc_data(C)#define WC_TIMED_EMPTY 0#define WC_TIMED_PIPE 1#define WCB_TIMED(T)
wc_bld(WC_TIMED, (T))
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Copyright (c) 2007 Clint Adams
Copyright (c) 2000 Sven Wischnowsky
(C) wc_data(C)#define WCB_FUNCDEF(O) wc_bld(WC_FUNCDEF, (O))
Copyright (c) 1996-1997 Peter Stephenson
(C) (wc_data(C) >> WC_LIST_FREE)#define WCB_LIST(T,O) wc_bld(WC_LIST, ((T) | ((O) <<
WC_LIST_FREE)))
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